LORAIN

MSP Radio Comm B3850 726-2855 Hangar 1 UPS For Trunked Repeater System

POWER EQUIPMENT

INSTRUCTIONS

INSTALLATION
OPERATION
MAINTENANCE

RELIANCE COMM/TEC

LORAIN MODEL WDA502B

ConstAC® UNINTERRUPTIBLE POWER SYSTEM

Spec. No. 5177-316 Serial No. 900

WARNING

This equipment generates, uses, and can radiate radio frequency energy and if not installed and used in accordance with the instruction manual, may cause interference to radio communications. As temporarily permitted by regulation it has not been tested for compliance with the limits for Class A computing devices pursuant to Subpart J of Part 15 of FCC Rules, which are designed to provide reasonable protection against such interference. Operation of this equipment in a residential, area is likely to cause interference in which case the user at his own expense will be required to take whatever measures may be required to correct the interference.

NOTE

For proper system operation, the external battery used for the ConstAC UPS must be grounded at the positive terminal in compliance with National Electrical Code Article 250 and/or local codes or practices. Refer to the Power Data sheet in this manual for further information.

LORAIN PRODUCTS

Division of Reliance Electric Co.

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MODEL WDA 302B; SPEC. NO. 5176-304-00 MODEL WDA 302B; SPEC. NO. 5176-306-00 MODEL WDA 502B; SPEC. NO. 5177-316-00

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General

1. INTRODUCTION

1.01 The ConstAC® Uninterruptible Power System (UPS) is an automatic standby AC power system which supplies uninterrupted 60 Hz AC power to a load. Under normal operating conditions, the UPS supplies regulated AC to the load using the commercial line as a source. At the same time, an integral battery charger in the UPS maintains the customer-provided battery in a state of full charge.

1.02 In case of commercial AC power failure, the inverter in the UPS immediately provides power to the load without interruption. The inverter is powered from the battery, and supplies the load with 60 Hz sine wave power.

1.03 After restoration of commercial AC power, the system control circuits determine acceptability of the AC line for use as the load power source. When the AC line is within acceptable voltage and frequency limits, the system automatically provides regulated AC to the load using the AC Line as a source. At the same time, the battery charger in the UPS recharges the battery.

1.04 Bypass circuits are provided in the UPS so the load can be connected directly to the commercial AC line. The system performs an automatic bypass if inverter output fails. Manual bypass can be performed if maintenance to the UPS is required.

1.05 This UPS contains various alarm circuits which continuously monitor input and output voltage levels and equipment functions. If an abnormal operating condition occurs, such as a failure in the system, low battery voltage, or loss of AC input power, an alarm indication is provided.

1.06 Meters are provided which indicate battery voltage, battery charge or discharge current, and AC output voltage and current.

Accessories

1.07 A rear dress panel is available for either factory or customer installation. This panel covers exposed mounting hardware on the rear panel of the system.

2. INSTALLATION

Location

2.01 This UPS should be located in a well ventilated area where the ambient air temperature range does not exceed the limits of 0° to 50° C (32° to 122° F). Insure that unrestricted air flow is provided at the front and top of the cabinet.

Wiring

- 2.02 Refer to the Power Data Sheet for wire sizes and installer connections. All external wiring, including input, output, and interconnecting wiring between the system and remote status panel is supplied by the customer.
- 2.03 The external battery used as the DC source for this UPS should be dedicated to that use only with no other DC loads connected. Additionally, only one ConstAC UPS should be connected to any Particular battery.
- 2.04 If the battery used for this UPS is normally charged by an external rectifier, such as in a central office the charging output of this UPS should be disabled by installation of Charge Eliminate Option D. Observe the foliowing procedure:
 - a) Connect a 16 ga. jumper wire to TB4, terminals 8 and 9.
 - b) Refer to paragraph 3.05, and perform System Startup procedure.

- c) Locate the current limit adjustment potentiometer, R46, on the control circuit card mounted on the inside of the system door. R46 is the bottommost potentiometer at the edge of the circuit card.
- d) Monitor battery charging current at the UPS meter panel or use a clampon ammeter or current probe around one of the battery leads in the UPS.
- e) Adjust potentiometer R46 counterclockwise until the system charging circuits deliver no output current to the battery.

Installation of Option (D) is now complete. The external battery is charged by its dedicated rectifier, and the battery charging circuit in the UPS will not provide any output current.

External Alarms and Control

- 2.05 Terminals 1 through 7 of TB4 provide alarm outputs and a means for connecting a remote float/equalize switch. The alarm outputs at terminals I through 5 are extended relay contacts rated at 1.0 ampere, 30 VDC or 120 VAC. A toggle switch or other type of the customer's choice, rated at 3 amperes, 120 VAC, may be connected at terminals 6 and 7 if control of the float and equalize functions is desired at a remote location. Further descriptions of the alarm and control functions follow.
- 2.06 Charge/Discharge Alarm. One set of relay contacts is available at TB4. terminals I and 2, to operate an external charge/discharge alarm provided by the system user. An open circuit is available when the system is charging the battery. A closed circuit is available when the battery is discharging to provide the inverter with input power.
- 2.07 Bypass/Inverter Alarm. A set of relay contacts is available at TB4, terminals 3 through 5, to operate an external bypass/inverter alarm provided by the system user. When system bypass circuits are activated,

supplying the load directly from the bypass line, an open circuit is present at terminals 3 to 4 and a closed circuit is present at terminals 4 to 5. When system output supplies the load through the AC regulator, using commercial AC input or the inverter as the main AC source, a closed circuit is present at terminals 3 to 4 and an open circuit is present at terminals 4 to 5.

2.08 Remote Float/Equalize Control. Connecting a SPST switch to TB4, terminals 6 and 7, allows the system user to control battery float and equalize functions from a remote location. With the FLOAT/EQUALIZE switch on the system front panel set to the EQUALIZE position, the battery may be charged at equalize voltage by opening the external switch. Closing the external switch provides float voltage to the battery.

3. OPERATION

General

3.01 Under normal conditions with acceptable AC and DC inputs, operation of the UPS is automatic after starting the system and setting all controls to the desired positions. Normally, the commercial AC line supplies the load through a filter and regulator circuit while the battery charger in the UPS float charges an external 24-cell battery. If the AC line fails or voltage or frequency exceeds preset limits, the system inverter automatically provides load power. The source transfer occurs with no break in power.

3.02 Provisions have been made in this system so a bypass Circuit can be activated to supply load power while the UPS regulator and inverter circuits are turned off. A synchronous transfer of load power occurs when the bypass circuit is activated.

Controls and Indicators

3.03 The following controls and indicators are accessible at the front of the UPS for convenient system operation.

- the inverter can be applied or removed through use of this switch.
- b) Mode Selection Switch: This four position rotary switch allows selection of UPS operating mode. The switch positions and modes of system operation are explained as follows:

1) BYPASS SUPPLYING LOAD

- a. INVERTER OFF: AC line power is connected to the load through a bypass contactor. All other major circuits are turned off.
- b. INVERTER ON: AC line power is connected to the load through a bypass contactor. Inverter and regulator circuits are operating but not supplying load power. The battery charging circuit in the UPS provides charging current to the external battery.

2) REGULATOR SUPPLYING LOAD

- a. AUTOMATIC: When the AC line is within acceptable voltage and frequency limits, the static switch connects line power to the AC regulator circuit. Filtered, regulated AC is supplied to the load while the battery charging circuit charges the external battery. The inverter operates in a standby mode, ready to provide load power if the AC line becomes an unacceptable power source.
- b. INVERTER ON: The inverter supplies power to the load, obtaining input power from the battery.

- c) FLOAT/EQUALIZE Switch: This rocker switch is located on the front panel of the UPS, and allows selection of either float or equalize voltage applied to the battery.
- d) Switches \$2 and \$3: These switches are located on a panel at the right side of the cabinet, behind the front door. Switch \$2 connects commercial AC power to the bypass circuit, and \$3 connects power to the static switch.
- e) Switch \$5: This rocker switch is located on the front access door and permits the charging of the input filter capacitors.
- f) FLOAT ADJ/EQUALIZE AD3 Potentiometers: These controls are accessible through the front panel, and are located immediately above the FLOAT/EQUALIZE switch. The settings of these potentiometers determine the float and equalize voltages applied to the battery.
- g) A number of light emitting diodes (LED's) indicate the status of various inputs, outputs, and portions of the system circuitry. An explanation of each indicator follows:
 - I) AC LINE NORMAL: This green indicator illuminates when the AC line is within acceptable voltage and frequency limits.
 - STATIC SWITCH CLOSED: When AC line power is applied to the regulator through the static SWITCH, this green indicator illuminates.
 - 3) BYPASS CLOSED: If AC line power is applied to the load through the bypass line, this red indicator illuminates.

- 4) REGULATOR Output CLoSED:
 This green indicator illuminates
 during normal system operation
 when regulated AC power is available to the load.
- 5) POWER ON: During normal system operation or if the bypass line is closed, this green indicator illuminates to signify that AC output power is applied to the load.
- 6) INVERTER/CHARGER ALARM:
 This red indicator illuminates if a failure occurs in either the inverter or charger circuits of the system.
- 7) DS1: Illuminates to indicate state of charge during startup procedure.

8) **BATTERY Status**

- a. <u>CHARGE</u>: When the system battery charger supplies current to the battery, or the battery is not discharging during normal system operation, this green indicator illuminates.
- DISCHARGE: This red indicator illuminates whenever the battery begins discharging for any reason. Usually, battery discharge occurs when the system inverter supplies load power.
- c. LOW VOLTAGE SHUTDOWN:
 This red indicator illuminates if system shutdown occurs after an extended discharge period depletes battery reserve.
- **3.04** Metering: Meters are provided on the UPS front panel to indicate battery and AC output status. If provided, the meters indicate voltages and currents as follows:

a) BATTERY Status

- 1) <u>DC VOLTS</u>: This meter indicates battery voltage.
- 2) DC AMPS: This meter indicates charge and discharge current supplied-to or demanded from the battery, respectively.

b) LOAD Status

- 1) AC VOLTS: This meter indicates AC output voltage applied to the load.
- 2) AC AMPS: This meter indicates
 AC output current drawn by the load.

System Startup

3.05 Observe the following procedure when starting the UPS, either initially or subsequent to any shutdown.

- a) Place DC input switch to the OFF position.
- b) Insure that AC and DC inputs are applied to the UPS.
- c) Place the mode switch to position BY-PASS SUPPLYING LOAD-INVERTER OFF.

DANGER

Hazardous AC voltages capable of producing severe, perhaps fatal electrical shock are present at system components throughout the cabinet. When performing the next step, exercise caution that contact is not made with any exposed electrical termination.

- d) Loosen three retaining screws, and open the front door of the system cabinet. Locate switches \$2 and \$3, and place both switches to the ON position.
- e) Close the front door and tighten three retaining screws. If commercial AC power is applied to the system input, power is now available to the load through system bypass circuits.
- f) Depress and hold switch \$5 located on the front access door to the CAP CHARGE position.

NOTE

DS1 illuminates brightly initially, then slowly dims as the input filter capacitors charge. Hold switch S5 in the CAP CHARGE position until DS1 becomes dim or nearly extinguished, then proceed to the following steps.

g) Release switch \$5 and immediately place the DC input switch to the on position.

CAUTION

When operating mode Switch \$4, wait five seconds before proceeding to the next switch position to allow operation of the UPS to stabilize. Failure to observe this procedure may result in damage to the UPS.

h) Place the mode switch to position REGULATOR SUPPLYING LOAD-AUTOMATIC. Initially, the inverter circuit operates from battery power as evidenced by illumination of the BATTERY DISCHARGE indicator. After a one minute time delay, the system automatically switches to the AC line as the main source if 'the commercial AC line is within acceptable voltage and frequency limits and begin charging the battery.

- i) Approximately three minutes after inverter circuit start-up, the load will be sychronously transfers from bypass input power to a regulated, filtered output power as indicated by the illumination of the green REGULATOR OUTPUT CLOSED L.E.D. indicator.
- j) After the UPS accepts the AC line as the main power source and begins sup plying regulated, filtered output to the load, check all status indicators on the system front panel. All green indicators should be illuminated, and all red indicators should be extinguished. Any red indicators illuminated show an alarm condition which should be investigated and corrected. If necessary, refer to the Troubleshooting section of these instructions.

System Shutdown

3.06 System shutdown should be accomplished only when necessary; for example, if maintenance is required or if prolonged AC power failure has depleted battery reserve. Under normal conditions, the UPS should remain in operation after startup, regardless of load requirements for power. The operational features designed into the UPS make it unnecessary to start and stop the system on a daily basis. Should a system shutdown become necessary, observe the following proce-Perform Steps a) through c) only if UPS shutdown is necessary following total power with failure commercial AC batterv reserve depletion. Perform Steps a) through c) if complete system shutdown with removal of all power is desired.

CAUTION

When operating mode Switch S4, wait five seconds before proceeding to the next switch position to allow operation of the UPS to stabilize. Failure to observe this procedure may result in damage to the UPS.

- a) If the mode switch is in either of the REGULATOR SUPPLYING LOAD positions, place the mode switch to the BYPASS SUPPLYING LOAD-INVERTER ON position.
- b) Place the mode switch to position BY-PASS SUPPLYING LOAD-INVERTER OFF. If commercial AC Power is still applied to the input of the UPS, power is available to the load through the system bypass circuit.
- c) Place the DC INPUT switch to the OFF position.

DANCER

Although the inverter, regulator, and battery charger circuits are not operating, AC power may still be applied to the system input. If so, AC voltages capable of producing severe, perhaps fatal electrical shock are still present at various system components.

- d) If the load is turned off or can otherwise tolerate complete loss of AC power, the UPS should be totally deenergized before performing any maintenance within the system cabinet. Perform the following steps:
 - Loosen three retaining screws, and open the front door of the system cabinet. Locate switches 52 and \$3.
 - 2) Place both switches to the OFF position.
 - Open the external AC and DC disconnect devices to remove all power from the UPS.
- e) System shutdown is now complete.

Manual Load Transfer

3.07 Under normal conditions, the system operator can choose one of two power sources for the load; namely, the commercial AC line used directly, or the regulated AC output of the system using the AC line or inverter as the primary source. Usually, the regulated AC output of the system is used for load power while the, UPS operates in the AUTO-MATIC mode. If necessary, a synchronous transfer between power sources without a break in power supplied to the load can be accomplished under the following conditions:

- a) The following indicators should be illuminated or extinguished as specified:
 - AC LINE NORMAL: illuminated or extinguished
 - 2) POWER ON: illuminated
 - 3) INVERTER/CHARGER ALARM: extinguished
 - 4) BATTERY CHARGE: either illuminated or extinguished.
 - 5) BATTERY DISCHARGE: either illuminated or extinguished
 - 6) LOW VOLTAGE SHUTDOWN: extinguished

NOTE

The state of the BATTERY CHARGE and DISCHARGE indicators depends on the mode of system operation prior to load transfer; however, if the BATTERY DISCHARGE indicator has been illuminated for an extended period of time, transfer to inverter output should not be attempted.

- b) Check the indicated value on the DC VOLTS meter. Battery voltage must be greater than 42 VDC before performing a load transfer since the inverter supplies load power momentarily. This step may be disregarded if the BATTERY CHARGE indicator is illuminated.
- c) If system indicators meet the conditions in Step a) and other system operating conditions are presertioned, place the mode switch to the desired location under BYPASS SUPPLYING LOAD or REGULATOR SUPPLYING LOAD.

CAUTION

When operating mode Switch S4, wait five seconds before proceeding to the next switch position to allow operation of the UPS to stabilize. Failure to observe this procedure may result in damage to the UPS.

d) The system transfers to the selected power source automatically after the mode switch is rotated to the desired position.

CAUTION

If the inverter is selected as the main power source (REGULATOR SUPPLYING LOAD-INVERTER ON) but the battery is not charged from an external dedicated rectifier, the UPS will operate until battery reserve is depleted and the low voltage shutdown is activated. The inverter may be used as the primary power source, but battery charge must be maintained for prolonged operation in this mode.

Automatic Shutdown

3.08 The UPS is protected against excessive DC input current by a low voltage shutdown circuit which disables inverrer operation when battery voltage decreases to 42 VDC. Battery voltage

can decrease to this level following prolonged commercial AC failure or if the system battery charger circuits fail for any reason. The system may be restored to normal operation following a low voltage shutdown according to the following procedure:

a) Insure that the battery charging circuit in the UPS is functional and that the commercial AC line is within normal limits. Check the DC INPUT switch to insure it is placed in the ON position. If not see Section 3.05.

CAUTION

When operating mode Switch 54, wait five seconds before proceeding to the next switch position to allow operation of the UPS to stabilize. Failure to observe this procedure may result in damage to the UPS.

- b) Place the mode switch to position BY-PASS SUPPLYING LOAD-INVERTER ON. Power is available to the load through the bypass circuit.
- c) The battery charging circuit recharges the battery in this mode of operation. The battery is fully recharged from a low voltage condition in approximately 2-1/2 hours when a 30-minute reserve battery is used.

Fuse Types and Sizes

3.09 The following tables list the types and ratings of fuses **Used** in the UPS. If replacement becomes necessary, use only the same type and size as listed, or equivalent, for proper equipment protection. In addition to the equipment fuses listed in Tables 3-1 and 3-2, the AC and DC inputs should be fused in accordance with the recommendations in the Power Data sheet.

Ref. Desig.	Туре	Size (Amps)
F1, F2, F5	Bussmann AGC3	3
F3	EON 100	100
F4	Gould-Shawmut A50P150	150
F6	Bussmann MDL2	2
F7, F8, F9	Bussmann AGC 1	l

Table 3-1. WDA 302B Fuse Type and Sizes

Ref. Desig.	Туре	Size (Amps)
F1, F2, F5	Bussmann ACC3	3
F3	Bussmann NON 150	150
F4	Could-Shawmut A 50P300	300
F6	Bussmann_MDL2	2
F7, F8, F9	Bussmann ACC 1	

Table 3-2. WDA 502B Fuse Types and Sizes

4. ADJUSTMENTS

General

4.01 Various control potentiometers are mounted on the main control circuit card in this UPS: however, only the float and equalize voltage adjustment potentiometers are made accessible for user adjustment. Procedures for adjusting the float and equalize voltage levels are given in this section of the manual, and only these adjustments should be attempted by the user. Do not attempt to perform any adjustments not described in this section. Disturbing the settings of any internal factory-adjusted potentiometers will result in degraded system performance or complete system failure. The float and equalize voltage controls are accessible through the front panel, and are located near the FLOAT/EQUALIZE switch. The equipment required for adjustment is listed as follows:

Test Equipment
Digital Voltmeter

Description
Fluke Model 8022A
or equivalent

Insulated Screwdriver or alignment tool

CC Electronics Type 8728A or equivalent

DANGER

Voltage adjustments are performed while the system is operating. Hazardous voltages capable of producing severe, perhaps fatal electrical shock are present at all components during system operation. Exercise caution that contact is not made with any electrical termination, especially when connecting meter test probes.

Remove watches, rings, and other jewelry before connecting test instruments and making adjustments.

To further avoid the possibility of electrical shock, use only an insulated screwdriver or the alignment tool recommended in the test equipment list when adjusting voltage control potentiometers.

4.02 The following conditions must be satisfied before float and equalize voltage levels can be adjusted.

a) Commercial AC power must be present and within normal limits. Indicator AC LINE NORMAL should be illuminated.

CAUTION

When operating mode Switch 54, wait five seconds before proceeding to the next switch position to allow operation of the UPS to stabilize. Failure to observe this procedure may result in damage to the UPS.

- b) The mode switch must be set to one of two positions, either BYPASS SUP-PLYING LOAD-INVERTER ON or REGULATOR SUPPLYING LOAD-AUTOMATIC.

<u>NOTE</u>

If Option (D) is installed, disregard the float and equalize voltage adjustment procedure. Installation of this option disables the system battery charger circuits when an external, dedicated rectifier is used to charge the UPS battery.

4.03 Float Voltage Adjustment

a) Loosen three retaining screws, and open the front door of the cabinet.

DANGER

Hazardous voltages are present at all components of the system. Exercise caution that contact is not made with any electrical termination, especially in the next step.

- b) Adjust the voltmeter or multimeter to indicate DC voltage within the range of 50 to 60 volts. Connect the meter test leads to the positive and negative terminals of switch \$1. Observe correct polarity.
- c) Place the FLOAT/EQUALIZE switch on the UPS front panel to the FLOAT position. If a remote FLOAT/EQUALIZE switch is connected to the UPS at TB4, terminals 6 and 7, insure it is placed to the open (EQUALIZE) position. The remote switch must remain in this position during the entire adjustment procedure.

d) While observing the multimeter indication, adjust potentiometer FLOAT AD3 for the proper float output voltage, as listed in Table 4-I.

WARNING

The voltages listed in Table 4-1 are those required for 24-cell standard lead-acid batteries only. If a different type of battery is used for the UPS, refer to battery manufacturer's charging data before adjusting float voltage. Incorrect charging voltage can either destroy a battery or result in insufficient reserve.

Battery Type	Volts per Cell	Total Float Voltage (24-ceils)
Standard Lead-Acid	2.17	52.08V

Table 4-1. Typical Float Voltages

 e) After setting the float output voltage, proceed with the equalize voltage adjustment procedure which follows.

4.04 Equalize Voltage Adjustment

- a) Check, and if necessary, adjust the float output voltage as outlined in paragraph 4.03
- b) Place the FLOAT/EQUALIZE switch on the UPS front panel to the EQUAL-IZE position.
- c) While observing the multimeter indication, adjust potentiometer EQUAL-IZE AD3 for the proper equalize output voltage, as listed in Table 4-2.

WARNING

The voltages listed in Table 4-2 are those required for. 24-cell standard lead-acid batteries only. If a different type of battery is used for the UPS, refer to battery manufacturer's charging data before adjusting equalize voltage. Incorrect charging voltage can either destroy a battery or result in insufficient reserve.

Battery Type	Volts per Cell	Total Equalize Voltage (24-cells)
Standard Lead-Acid	2.35	56.40V

Table 4-2. Typical Equalize Voltages

- d) After setting the equalize output voltage, return the FLOAT/EQUALIZE switch to the FLOAT position. Check for proper float voltage.
- e) Disconnect the meter test leads.
- f) Close the UPS front door, then tighten three retaining screws.

4.05 Low Voltage Shutdown Adjustment Procedure: Perform the following procedure to verify that the UPS shuts down if the DC input voltage drops to a value within the range of 41.9 to 41.5 volts DC. If necessary potentiometer R 85 may be adjusted to assure the proper low voltage shutdown point.

DANGER

Hazardous AC and DC voltages capable of producing severe, perhaps fatal electrical shock are present at system components throughout the cabinet. When performing the following procedure exercise extreme caution that contact is not made with any energized electrical termination.

CAUTION

When operating Switch S4, wait five seconds before proceeding to the next switch position to allow the operation of the UPS to stabilize. Failure to observe this procedure may result in damage to the UPS.

WARNING

If this UPS is used in a power system where a low voltage disconnect is set to disconnect the DC input power at 42.0 volts DC, the UPS low voltage shutdown should be reset to approximately 43.0 volts DC. This procedure should be performed to insure that the UPS is shut down before the removal of DC input power.

- a) Place the mode selector switch \$4 to the INVERTER OFF BYPASS SUPPYLINC LOAD position.
- b) Place the DC input Switch \$1 to the OFF position.
- C) Open the front access door and locate AC input fuse F9 and power supply fuse FI. Fuse F9 is located near the middle right side of the cabinet, while F1 is located near the middle left side of the cabinet.
- d) Remove fuses F1 and F9 from their respective fuse holders.

DANGER

When performing the following step the polarity of the external power supply is critical. Insure that the connection of the positive and negative leads from the power supply is correct before turning the power supply on, otherwise permanent damage to the control circuit card will result.

e) Locate AC input switches \$2 and \$3 and place them to the OFF position.

- f) Connect an external 0 to 52 volt DC power supply and a digital multimeter to the terminals located on the back of DC voltmeter MI. Observe the polarity indicated on the back of DC voltmeter M 1. Connect the positive lead from the power supply to the terminal labeled (+) and the negative lead of the power supply to the unlabeled terminal on M 1.
- g) Turn ON the external DC power supply and adjust the power supply until the DMM indicates 52.0 volts DC.
- h) Place the DC input circuit breaker CBI to the ON position.
- Place the mode selector switch S4 to the BYPASS SUPPLYING LOAD INVERTER ON position, the UPS should start.
- j) Lower the DC voltage from the external power supply until the UPS shuts down and DS7 (Low Voltage Shutdown LED) illuminates.
- k) Observe the voltage indicated on the DMM, the voltage indicated should be in the range of 41.5 to 41.9 volts DC. If the voltage indicated on the DMM is not within this range, place the selector switch \$4 to the BYPASS SUPPLYING LOAD INVERTER OFF position until DS7 extinguishes.
- 1) Increase the power supply voltage and place \$4 to the BYPASS SUPPLYING LOAD INVERTER ON position. Decrease the voltage from the power supply to the desired level. Adjust potentiometer R\$5 until the UPS shuts down and DS7 illuminates. Repeat Steps h) through k) as necessary until the UPS shuts down at the desired level.
- m) Place switch \$4 to the BYPASS SUPPLYING LOAD INVERTER OFF position. Place the DC input circuit breaker CB1 to the off position.

- n) Remove the power supply from the back of voltmeter M 1.
- Replace the fuses removed in Step d), into their respective fuse holders and place switches \$2 and \$3 to the ON position.
- p) Close the front access door, and perform the start-up procedure detailed in Paragraph 3.05, the UPS may now be returned to service.

5. CIRCUIT DESCRIPTION

General

5.01 The ConstAC Uninterruptible Power System contains inverter, battery charger, AC regulator, static switch, and bypass circuits as well as necessary control and alarm circuits. A selector switch mounted on the system front panel and connected to the main control circuits determines the mode of UPS operation, depending on the position chosen by the operator. Unless otherwise specified, this circuit description is presented under the assumption that the system is supplying power to the load in the AUTOMATIC mode.

Power Circuits

The power handling Circuits of the UPS are interrelated during normal operation to the extent that the AC line provides power to the load while serving as a source for the battery charging circuits. Simultaneously, the AC regulator insures that regulated, filtered AC is..applied to the load while the inverter circuit operates .in a., standby mode. A breakdown of circuit operation follows.

5.03 Static Switch. The static switch, shown in simplified form in Figure 5-1, consists basically of two silicon controlled rectifiers (SCR's) connected in inverse parallel configuration. These SCR's are triggered into conduction by firing pulses from the gate driver circuits so conduction occurs during alternate half cycles of the AC

line, thereby applying the complete sine wave to the AC regulator. When firing pulses are not applied, the SCR's block AC input which occurs, for example, when system bypass circuits are activated. Essentially then, the static switch comprises an electronic switch that enables the application or removal of AC line power to or from the AC regulator circuit.

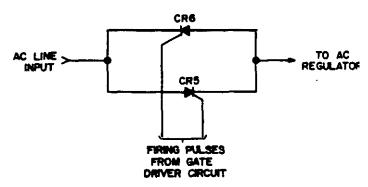


Figure 5-1
Static Switch Schematic Diagram

5.04 AC Regulator. A simplified schematic diagram of the UPS AC regulator circuit is shown in Figure 5-2. Transformer [] is represented as a saturable reactor, and the I-4 winding of T1 is shown as a series inductance. Under normal conditions, commercial AC power is applied to the AC regulator circuit through the static switch and inductor L3. The regulator circuit, comprising ferroresonant transformer T1 and associated resonating capacitors, essentially forms a tank circuit resonant at 60 Hz. A substantial amount of energy circulates in the tank circuit, and a portion of this energy is used to supply the load. Circulating energy is sufficient to compensate for line voltages slightly lower or higher than nominal voltage of 117 VAC; therefore, the AC output made available to the load is relatively constant.

5.05 Under normal line voltage conditions with power demands of the load satisfied, a small amount of energy in the AC regulator is still available to charge the external battery. This energy is magnetically coupled through the I-4 winding of T1, resulting in a

voltage which is rectified and applied to the battery. A DC regulator in the UPS control circuits determines the charging voltage and current applied to the battery.

5.06 Battery Charger and Inverter. A simplified schematic diagram of the battery charger and inverter circuit is shown in Figure 5-3. During normal system operation with commercial AC power applied to the AC regulator circuit, a portion of the energy available in the resonant tank circuit is magnetically coupled to the I-4 windings of T1. The resultant voltage induced on this winding is rectified by diodes CR5 and CR8. This DC voltage is applied to the battery through a DC filter circuit. Positive battery return connects to the center tap of the I-4 winding of T1.

5.07 The inverter circuit of the UPS operates continuously and in synchronism with the commercial AC line frequency. The inverter circuit does not supply power to the load when the AC line is used as the primary source. Upon AC line failure or the occurrence of an out of limits condition, the inverter automatically supplies power to the load through the AC regulator. The inverter operates in the following manner: pulses generated in the control and gate driver circuits are applied to one SCR, for example, CR7. This SCR switches from a blocking state to conducting state, allowing current to flow through the 3-4 winding of T1, thereby generating the first half cycle of a square wave. At the same time, the commutating capacitor charges to a voltage level of approximately twice battery voltage.

5.08 At the proper time, gating pulses are applied to CR6, switching it from a blocking state to a conducting state. With both SCR's conducting simultaneously, the commutating capacitor discharges then recharges to a voltage level approximately twice that of battery voltage, but of opposite polarity of the previous charge. The commutating capacitor, diode CR8, and inductor L2 form a

resonant circuit which generates a commutating pulse of short duration. This pulse reverse biases CR7 which reverts to a blocking state.

5.09 With CR6 still conducting, current flows through the 1-2 winding of TI, thereby generating the second half cycle of a square wave. The alternate conduction of CR6 and CR7 under direction of the control circuits occurs 60 times per second which generates the 60 Hz square wave impressed on the I-4 winding of T 1.

5.10 Ferroresonant transformer T1 in the AC regulator circuit converts the square wave generated by the inverter into a sine wave of low harmonic distortion. The inherent magnetic characteristics of T1 protect the UPS from faults and short circuits at the output. Furthermore, transformer T1 isolates the DC inverter circuitry from the AC output circuits and supplies the correct output voltage to the load.

Control Circuits

5.11 The major portion of the system control circuits is contained on circuit card AI. Major subcircuits of AI include the phase detector, VCO, pulse generator and amplifiers, AC sense and debounce, battery charger regulator, bypass control, low voltage shutdown, and alarm circuits. The overall operation of each foregoing circuit is explained in the following text.

5.12 Phase Detector. This circuit detects the phase relationship of the commercial AC line. and inverter AC output, and controls system phase relationships and inverter frequency during the various modes of operation. An AC line reference signal, derived from the secondary winding of T2, is applied to squaring amplifier A IG 3. Resistor R1 and capacitor C2 shift the phase of the applied signal by 75 degrees. Inverter AC is applied to squaring amplifier A IG 2 after being phase shifted 165 degrees by R6, R30,

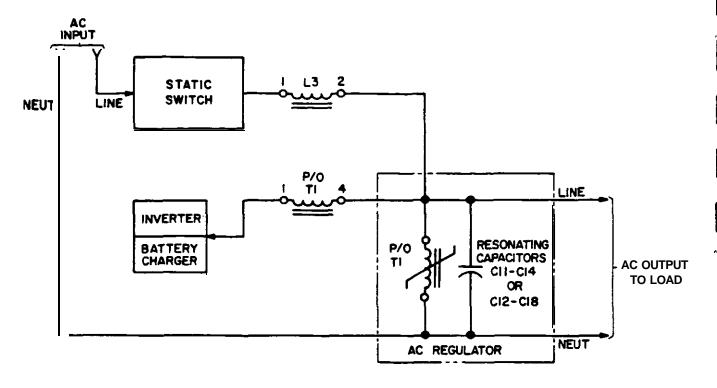


Figure 5-2. Simplified Schematic Diagram: AC Regulator

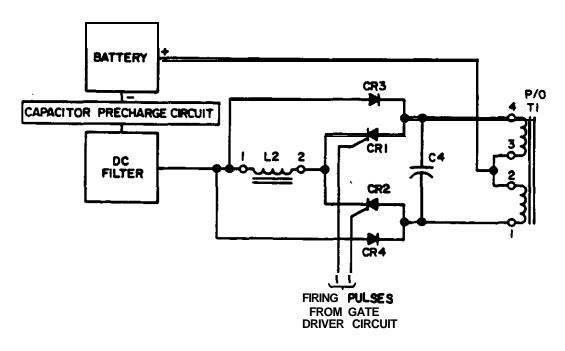


Figure 5-3. Simplified Schematic Diagram: Battery Charger and Inverter

C13, and C14. The outputs of these squaring amplifiers are applied to the inputs of exclusive • OR gate A5G4 which provides a square wave of variable duty cycle to level The output from AIGI is shifter AlGI. applied to the loop compensation circuit, comprised of A 3G I and associated components, through R20 and R21, and is controlled by analog switch A12. During normal system operation in the Automatic mode with an acceptable AC line, the output from AIGI is applied to the inverting input of A3G I, and analog switch A 12 is turned off. During operation in any other mode or if the AC line becomes unacceptable for any reason, Al2 is turned on by control signal FREERUN. One portion of Al2 clamps the junction of R20 and R21 to ground so the phase detector output signal from AIG1 cannot be applied to A 3G 1. A second section of Al2 connects R22 in parallel with C7 and R24 in the feedback loop of A3G1, thereby discharging loop filter capacitor C7. The output from the loop compensation circuit is applied to the voltage control oscillator (VCO) circuit through R27.

5.13 Voltage Controlled Oscillator. Level shifter A24G I and oscillator A2 comprise the VCO circuit which functions as the master clock for system control circuit timing. The basic operating frequency of **A2** determined by CO, R23, and Temperature compensation is provided by CR9 and R29. Operating frequency is modified by A24G1 which functions as a current source or sink depending on system operating conditions during the AUTOMATIC mode of operation. Whenever the inverter is operating during Periods of unacceptable AC line power or total lack of AC input, the output from A24G1 is held at essentially zero volts relative to pin 8 of A2 since the output from the phase detector circuit is damped to ground by A12. An internally generated reference voltage available at pin 10 of A2 is applied to the noninverting input of A 24G L through voltage, divider resistors RI2 and R25. At this time, the free running output

frequency of A2 is determined by the combined timing resistance of R23 and R28. The 246 KHz output square wave from A2 is applied to both the debounce circuit and the 120 Hz/3KHz decoder circuit.

5.14 Pulse Generator And Amplifiers. The pulse generator and amplifier circuits produce- the gating pulses used by the inverter and static switch SCRs (silicon controlled rectifiers). The pulse trains or groups are applied to the SCRs as a means of controlling the conduction times of these devices. Two decoder circuits and an enable circuit control the pulse amplifiers. The outputs from all amplifier transistors are applied to the gate driver transformers on circuit card A2.

5.15 The 246 KHz output from oscillator A2 is applied to the clock input of counter A15. This integrated circuit divides the input frequency and supplies two outputs. The Q4, O5, and Q6 outputs are directly connected to the inputs of AND gate Al 1G3 which outputs a continuous 3KHz pulse train. follower Q1 serves as a current amplifier for this 3 KHz signal, and drives the inputs of all pulse amplifier stages. The Q11 output of A15, a 120 Hz square wave, is applied to Dtype flip-flop A18 through inverter A17G5. Flip-Flop A18 produces two 60 Hz square waves, 180 degrees out of phase, at its Q and outputs. These outputs alternately serve as current sources and sinks to control the conduction of Q10 and Q11 through the driver transistors of A21. For example, if output Q is low (ground) and output \overline{Q} is high, diode CR38 is reverse biased and CR40 is forward biased. The 3KHz pulse train applied to R 118 is shunted to ground through CR40, and Q 11 produces no pulse group output to its associated driver transformer. The 3 KHz pulse rain applied to R 117 is allowed to pass through to one of the driver transistors in A21 since CR38 is reverse biased. Output transistor Q10 amplifies this pulse group and it the associated driver applies to transformer. At the next transition of the

120 Hz input to A18, the outputs charge state so Q is high and \overline{Q} is low. Under this condition, Q10 produces no pulse group outputs which Q11 applies an output to its driver transformer. The outputs from Q10 and Q11 drive the gates of the inverter SCRs with 3 KHz pulse groups interrupted at a 60 Hz rate. These gate drive signals control the basic operating frequency of the inverter power circuits.

5.16 Output transistors QS and Q9 and associated driver circuits function in a manner similar to that described for Q10 and Q11; however, Q8 and Q9 provide continuous 3 KHz pulse train outputs for the static switch SCRs rather that interrupted pulse groups. Cating pulse outputs for the static switch SCRs are enabled or disabled by a clamp transistor in A13, coupled to diodes CR34 and CR35. When control signal SSOFF applied to R 137 is low, transistor Al3 is biased into cutoff which reverse biases CR34 and CR35. This same control signal simultaneously forward biases Q5 to energize the coil of K3. The relay contacts apply power to the CHARGE indicator, remove power from the DISCHARGE indicator, and open the discharge alarm contacts. CR34 and CR35 are reverse biased, the 3KHz to both is applied pulse train transistors of A21 through R115 and CR47. and to R116 and CR48. Associated output transistors Q8 and Q9 amplify the applied pulse trains and couple them to the gate circuits of the static switch SCRs through separate driver transformers. During periods of inverter only operation, the gating pulses to the static switch SCRs are disabled by applying a high SSOFF control signal to transistor A 13 through R137. This control signal forward biases transistor A13 so the junction of R115 and CR47 is clamped to ground through CR34, and the junction of R 116 and CR48 is clamped to ground through This clamping function disables application of the 3 KHz pulse trains to Q8 and Q9 so the static switch SCRs remain in a blocking mode. Simultaneously, the high

SSOFF control signal biases Q5 into cutoff, deenergizing the coil of K3. The associated contacts apply power to the DISCHARGE indicator, remove power from the CHARGE indicator, and close the discharge alarm contacts.

5.17 Half-Cycle Start Latch. This circuit functions at the moment of inverter startup and momentarily clamps all gate drive signals to the inverter and static switch SCRs to provide a programmed starting sequence. The half-cycle start latch consists of A18, transistor A13. and associated At the moment the inverter components. circuits are turned on, several circuit functions occur simultaneously. The reset input at pin 10 of A18 is held high for 27 milliseconds. determined by the time constant of R138 and C22. The resulting logic high on the Q output at pin 12 of A18 biases the associated transistor of Al3 into saturation. With transistor A 13 conducting, diodes CR34-CR37 and CR39 are forward biased so the 3 KHz gating pulses for the inverter and static switch SCRs are clamped to ground.

When the inverter is turned on, control **'5.18** signal INVON is switched to a +15 volt level. This signal is applied to the D input at pin 9 of A18 through CR41. A8 this time, the Q output at pin 12 of A18 is held at logic high to bias transistor Al3 into saturation to maintain the clamp on the gating signal drive The next positive going leading edge of the 120 Hz signal is clocked into A18 at pin II, forcing the O output at pin 12 low. This signal biases transistor A13 off during the positive portion of the first cycle of the 120 Hz signal, unclamping the gate pulse driver circuits. At the same time, static switch driver transistors Q8 and Q9 remain clamped since control signal SSOFF applied to transistor A 13 through R 137 is high. The Q output at pin 2 of A18 remains low to clamp Q10 while the Q output at pin I goes high to reverse bias CR40, The 3 KHz gating pulse train is applied to transistor A21 and Q1 I at

this time. When the trailing edge of the first cycle of the 120 Hz signal is clocked into pins 3 and II of A18, the Q output at pin 1 goes low to clamp transistor A21 and Q11, and the output at pin 12 remains low to keep clamp transistor A13 biased into cutoff. With this transistor biased off, the clamp is removed from-all gate driver input lines, and the Q and Q outputs at pins I and 2 of A18 regain control of the inverter SCR gate driver transistors. In overall result, this circuit allows one inverter SCR to conduct for only 8.33 milliseconds, with all other SCRs clamped in a nonconducting state. first half-cycle operation occurs only when the inverter is first started, and has no further affect on circuit operation after that time unless the inverter is turned off by either manual or automatic means.

5.19 Phase Boost Circuit

During a synchronous transfer of load power from the AC line to inverter while the system is loaded at 50% or more of rated output power, the phase boost circuit applies a one millisecond pulse to oscillator A2 to momentarily increase its output frequency. This frequency increase tends to increase output voltage and minimizes voltage sag of the output sine wave while the ferroresonant regulator circuit stabilizes. The phase boost circuit consists of A10, A14G4, Q12, Q13, and associated components. During normal system operation, the output from A5G4 is applied to averaging comparator A14G4 at the inverting input. If system output load does not exceed 50% of rated power, the phase difference between the AC line and inverter AC results in a variable duty cycle square wave input to A14G4 that results in a high output at pin 14. This signal, applied to C29 through R104 and CR43, is of sufficient amplitude to keep C29 fully charged. This voltage level biases Q12 into saturation which removes forward bias from Q13. With Q13 biased into cutoff, a logic high is applied to the reset input of Al2 through R110. The Q output is a logic high under this condition' and reverse biases CR45 so the output frequency of oscillator A2 is not affected.

5.20 If system output is loaded at more than 50% of rated power, a larger phase difference between the AC line and inverter AC occurs. The variable duty cycle square wave output from A5G4 applied to A14G4 is sufficient to drive the output of averaging amplifier low. Capacitor C 29 discharges and removes forward bias from O12. This allows application of forward bias to Q13 through R106, resulting in application of a logic low to the reset input of A10. If the system performs a transfer of load power under this condition, a logic high SSOFF signal is applied to the clock input of A10. The output immediately undergoes transition from logic high to logic low, and the Q output of A10 undergoes transition from logic low to logic high. The Q output signal serves as a bias source for Q12, and transistor turns on after C29 charges sufficiently to exceed the breakover voltage of CR44. The time delay in charging is approximately one millisecond. With Q12 biased into saturation, Q13 is biased into cutoff and a logic high is again applied to the reset input of A10. At this time, outputs Q and Q of A10 undergo transition and revert to their former logic levels. The timing constants of R105 and C29 result in a \overline{Q} output logic low pulse of one millisecond duration. This pulse forward biases CR45 for its duration, and increases the frequency of A2. This results in increased inverter operating frequency approximately one millisecond to force system output voltage up during the transfer of load power. After the $\overline{\mathsf{Q}}$ output of A10 undergoes transition from logic low to logic high, the phase boost circuit has no further affect on system operation.

5.21 Battery Charger Regulator System DC output voltage and current are regulated to appropriate values for charging the external battery. Separate amplifier and control circuits are used for voltage and current regulation, but only one loop compensation circuit is used for maintaining circuit stability. The output of the regulator circuit is switched into or out of the phase detector circuit under automatic control of

the CHARGE signal. The output of the regulator circuit, consisting of A4G2, A4G3, A12, and A25, modifies the phase difference between inverter AC output and the AC line to control the amount of power available for charging the battery.

5.22 Battery voltage is applied to the inverting input of error amplifier A4G2 through a voltage divider consisting of R35, R38, and R40. The settings of R38 and R40 determine the levels of sampled voltage applied to A4G2 for float and equalize modes of operation. The sampled battery voltage is reference voltage, against a compared applied at the noninverting input of A4G2, generated by CR74, R42, R43, and CR20. The output of A4G2 is the difference voltage appearing at the inputs, and is coupled into the loop compensation circuit through CR21. Amplifier A25 is used to amplify the shunt voltage to a usable level, and this voltage is used as a measurement of DC output current supplied to the battery. The setting of potentiometer R46 determines the current limit point. The output from A25 is coupled into the loop compensation circuit through R50 and R51.

circuit 5.23 The qool compensation contains amplifier A4G3 controlled by two analog switch A12. sections of CHARGE control signal determines the ON or OFF state of A12. During normal system operation with acceptable commercial AC power applied, a logic high CHARGE signal is applied to A17G3 and to the section of A 12 connected across the feedback network around A4G3, 'This section of Al2 is turned off (open) for normal operation. Inverter A17G3 inverts the logic high input signal so the section of A12 in the output line of A4G3 is turned on (closed). This allows the output of A4G3 to increase or decrease the control voltage input of A3G1, thereby serving to control the operating frequency of the VCO The small changes in frequency circuit. translate into changes of output current applied to the battery while the voltage is maintained at a constant value. If the

commercial AC line fails or becomes unacceptable for any reason, the CHARGE control signal changes state to a logic low. The section of Al2 in the output line of A4G3 opens so the regulator circuit output does not affect operation of the VCO circuit. Simultaneously, the section of A 12 connected across the feedback loop of A4G3 closes to discharge loop compensation capacitor C 17. Under these operating conditions, the battery charger regulator circuits are disconnected from the phase detector and VCO circuits while the inverter is providing system output power.

5.24 AC Sense The components comprising the AC sense circuit enable synchronization with AC line frequency, and also determine acceptable voltage limits at which the AC line is used as the primary load power source. The input from stepdown transformer 12 is applied to diodes CR1 and CR2 through resistors R8 and R9. The 120 Hz pulsating DC is applied to squaring amplifier A4G1 through a voltage divider consisting of R64 and R68. The setting of potentiometer R64 determines the low limit of acceptable AC line voltage . The square wave output from A4G1 drives a digital filter consisting of NAND gates A22G 1-A22G3 and counter A23 which filters any noise or transient pulses with durations less than or equal to 130 microseconds. The output of counter A23 drives the input of retriggerable monostable multivibrator A7 through NAND gate A22G4 used as an inverter. The output pulses from A7 serve as a clock input to flipflop A10, and are applied to exclusive - OR gate A5G3 which, in conjunction with R72 а C15. provides time delay of approximately 7 microseconds. These delayed pulses are applied to inverter A6G6 which provides a reset signal to timer A8. The noninverted output of A5G3 is also used as a reset signal in counter A9. Timer A8, synchronized to the logic low output pulses of A 5G 3, provides an output to pin 1, the clock input, of A9. The Q2 output of A9 provides a count to the data 'input of A10 through inverter A6 G5, When AC line frequency is within normal limits, the Q output at pin I of AlO is a logic high. If AC line frequency is out of tolerance, A9 continues counting beyond the Q2 output until the Q7 output undergoes transition from low to high. This output is applied to the data input of A10 through R76 to force the Q output at pin 1 low. If inverter only operation is selected, control signal INV ONLY is applied to the reset input of AlO through CR46 to force the Q output of pin I low. The logic high signal at the Q output of A10, under normal AC line conditions, forward biases driver transistor A13 through R82 to illuminate the AC NORMAL indicator. This same output also functions as one of the control signal inputs to AlIGI in the debounce circuit, and is also applied to inverter A6G1. The output of this inverter is applied to A6G3 through a time delay circuit comprised of CR7, R33, R34, and C₈. The output of A₆G₃ is used as the FREERUN signal which controls operation of the phase detector and loop compensation circuits.

5.25 Debounce Circuit The debounce circuit provides a digital delay which allows a period of time to elapse before the AC line is used as the primary load power source. If the AC line becomes unacceptable at any time during the debounce routine, the circuit automatically resets and again performs the entire routine from the beginning. When the AC line is within acceptable limits, ripple counter A16 is enabled by a reset signal supplied by the AC sense circuit at the output of A6G1. A 60 Hz square wave, derived from the output of AC line squaring amplifier AIG3, is applied to AND gate AIICI. A logic low at the Q13 output of A16 enables gate AIICI through inverter A17G2, and A16 starts counting the number of square wave transitions after application of the reset signal. Approximately 68 seconds must elapse before transition of the Q13 output occurs and becomes a logic high. With the Q13 output high, the output from A17G2 undergoes transition to a logic low which inhibits the 60 Hz square wave output from

AIICI. Further counting by A16 is thereby inhibited to complete the debounce routine. If the AC line becomes unacceptable during the 68 second counting period, a reset signal from the AC sense circuit is applied to A16 to restart the counting sequence from the beginning. Following successful completion of the debounce routine, the SSOFF control signal output from A 17G2 becomes a logic low to bias clamp transistor AI3 off. This enables application of the 3 KHz gating signals to driver transistors Q8 and Q9 which control the static switch SCRs, gating these devices into conduction.

Bypass Control, Inverter Fail, and Low Voltage Detectors. The bypass enable circuits control operation of the bypass and generate various signals used in other circuits on the control card. The bypass enable circuits respond to OFF or BYPASS commands provided by the mode selector switch, and to control signals generated by the low voltage shutdown and inverter failure detectors. **During** normal system operation with the REGULATOR LOAD/AUTOMATIC mode SUPPLYING selected, relay K1 is energized to provide a BYPASS ENABLE output signal and a closed bypass alarm contact. This signal controls the state of the output contactor so regulated system AC output, rather than the commercial AC line itself, supplies the load. Under these conditions, the static switch SCRs are enabled, the inverter AC output is locked in synchronism with the commercial AC line, and the battery charger regulator circuits are enabled so the system charges the external battery.

5.27 If a failure occurs in the inverter circuits resulting in application of a fuse alarm signal to the inverter fail detector, the output of comparator A 14G 3 under goes transition from logic low to logic high. This signal reverse biases Q4, and the coil of K I is deenergized. Simultaneously, the output of A14G3 is applied to inverter A6G4 which illuminates the INVERTER ALARM indicator, and forward bias Q7. The INVFAIL control

signal output, derived from the collector of O7, applies a logic high reset signal to counter A16 in the debounce circuit. With the coil of K I de-energized, the bypass alarm contacts open, and the bypass enable control is removed to allow the bypass contactor to connect the bypass AC line, is unconditioned commercial AC power, to the load. Contacts 12 and 13 of KI close to illuminate the BYPASS ON indicator and apply a logic high input at pin 6 of A5G2. At this time, the static switch SCR gating signals are disabled, Q5 is reverse biased to deenergized the coil of K3, the DISCHARGE indicator is illuminated, and the discharge alarm contacts are closed.

5.28 If AC line power remains unavailable or unacceptable for extended periods of time, the external battery may become sufficiently discharged to activate the low voltage shutdown circuit. The shutdown circuit also activates the bypass control circuit, availability of the regardless of commercial AC line. Battery voltage is monitored by comparator A14G2 through a voltage divider consisting of R84-R87. The setting of potentiometer R85 determines the low voltage shutdown threshold. The output of A14G2 is normally a logic high when battery voltage is greater than the selected shutdown limit. This output signal reverse biases Q2, and is inverted by A17G4. The output from A17G4 is applied to the base of QI4 through CR57, R121, and RI46 to bias this transistor on. When Q14 is conducting, the coil of K2 is energized so its contacts apply operating power to all control circuits. Under these conditions, assume that inverter AC output is supplying load power.

5.29 If battery voltage decreases to the selected shutdown level, the output of A 14G 2 changes from a logic high to a logic low. This signal forward biases Q2 so the LOW BATTERY indicator illuminates. Simultaneously, inverter A17G4 provides a logic high output which is coupled to comparator A14G1 through CR58 and R120. The output of A14G1 changes from a logic

low to a logic high which reverse biases Q4. The output from A14G1 is coupled to Q4 through steering diode CR24. At this point, circuit operation proceeds in the same manner as if an inverter failure were detected; however, the INVERTER ALARM would remain extinguished. To prevent extremely deep battery discharge, the output of A17G4 is decoupled from Q14 by CR57. Reverse bias is applied to the base of Q14 through R160, and the coil of K2 is deenergized. The contacts of K2 change state when the armature is released to remove battery power from the control card.

If the system is manually placed in the bypass mode through use of the front panel selector switch, a logic high BYPASS control signal is applied to pin 5 of AX2. This same control signal is applied to comparator A14G1 through steering diode CR65 and R120. At this point, circuit operation proceeds in the same manner as if a low shutdown condition occurred: voltage operating power would not be however, removed from the control circuits, and inverter operation would not be stopped.

5.31 Start-up Sequence Control A special start-up sequence occurs after low voltage shutdown if the mode selector switch is placed to the INVERTER ON or AUTOMATIC position. When commercial AC line power is made available to the system diodes CR15A-CR15D, connected in a full wave bridge configuration, rectify the AC supplied by stepdown transformer T2. Capacitors C 18 and CI9 filter the DC output from the bridge, and +15 VDC is made available to the control circuits through normally closed contacts 3-4 of K2. At this time, bypass AC is made available to the load through the bypass contactor. With power applied to the circuits, the debounce circuit containing A I IG I and A16 begins the countdown routine. Control signals LOCK, LVS, and SSOFF are all logic high so A L IG I can count the 60 Hz transitions. After 64 seconds have elapsed,, the logic low outputs from Q9, Q10, Q11, and Q12 of Al6 forward

bias transistor A13, thereby applying the LVS INH signal to the low voltage shutdown detector, Circuit action ultimately forward biases 014 to energize the coil of K2, thereby switching battery power to the control circuits. If AC line power remains acceptable, the SSOFF control signal output from A17G2 becomes a logic low 68 seconds after the debounce routine begins, thereby enabling application of the gating pulses to the static switch SCRs. The system locks to the AC line, and the battery charger circuits begin recharging the regulator battery. The system is still maintained in the bypass mode to allow a period of time for battery recharge.

A timing circuit consisting of A17G1, A19, and associated components determine the time that the system charges the battery following low voltage shutdown subsequent system restart. This circuit maintains a logic high input to A14G1 through CR65 and CR83, derived from the output of A17G1, for approximately three minutes after application of commercial AC input power. When this output becomes a logic low, the output of A14G1 becomes a logic low to allow application of forward bias to 04 which energizes the coil of KI. Contacts 12-14 close, applying the BYPASS ENBL control signal to the bypass contactor. This contactor is energized to connect system AC output rather than bypass AC to the load. The start-up sequence ends at this point, and the system resumes normal operation.

5.33 Alarm Circuits The alarm circuits, most of which are contained in the foregoing description, form an integral part of the control circuits and consist of various comparators, amplifiers, and drivers which provide LED indications on the system front panel. The alarm circuits monitor voltage levels, current flow, and status of various portions of the control circuits, providing the operator with a visual indication of system status.

Gate Drivers and Power Supply

5.34 Circuit card A2 contains SCR gate driver isolation transformers and a dual voltage DC power supply. Each pulse transformer, T1 through T4, connects to a pulse amplifier transistor, Q8 through Q11, on the control circuit card. The transformers provide a means of coupling and isolation between the control circuits and the SCR gates in the inverter and static switch. The diodes connected across the primary winding of each transformer reset the core between pulse groups to prevent magnetic saturation of the core material.

5.35 The power supply on circuit card A2 provides regulated -15 and -30 VDC used in the control circuits. These voltages are derived from zener diodes CR13 and CR14 connected to the battery or rectified AC line through circuit card Al. Operational amplifier Al and series-pass transistor Q1 form a voltage regulator circuit which provides approximately 35 VDC to the primary windings of the pulse transformers. Battery or rectified AC line is also used as the source for this 35 volt supply.

Status Relays

5.36 The status relays on circuit card A3 are driven from the alarm and status signals generated by the control circuits. The control signal is applied to a relay driver transistor directly, or to the transistor through an inverter. When a control signal is applied to the base of a driver transistor, that transistor conducts and energizes a relay coil in its collector circuit. Relay contacts either apply or remove voltage at an LED which provides a visual alarm or status indication.

6. TROUBLESHOOTING

Introduction

6.01 The troubleshooting information in this section should be used as a guide in localizing failures in the UPS. Before at-

tempting any troubleshooting procedures, the UPS should be disconnected from the load and isolated from any other power equipment. In doing so, the system is isolated from any external equipment failures which may reflect as a trouble symptom.

DANGER

Unless otherwise specified, trouble-shooting procedures should be undertaken only after AC and DC power sources to the UPS have been disconnected. AC voltages used in this system can cause severe, perhaps fatal, electrical shock; therefore, service personnel should observe all precautions normally associated with maintenance and repair of electronic equipment.

Fault Isolation Chart Use

6.02 The fault isolation chart included in these instructions is designed to aid service personnel in locating possible failures in the UPS. The chart is in the form of a flow diagram containing interrogatory and directive statements in diamond-shaped and rectangular geometric figures, respectively. A yes or no response sufficiently answers each inquiry, and the servicing technidan should follow the respective YES or NO line of flow to the next inquiry or directive statement. The directive statements require that the technician perform an adjustment or test to verify certain operational conditions. If the line of flow terminates in a block entitled POSSIBLE CAUSES, the servicing technician should check each cause listed and take appropriate corrective action if a faulty component or assembly is located.

Testing Precautions

DANGER

Do not connect grounded test equipment to any energized component: otherwise, damage to the UPS can result. At the same time, a personnel shock hazard exists since the chassis and case of ungrounded test equipment is energized to the same voltage level as the circuit or component under test. Servicing personnel must exercise caution that simultaneous contact is not made with the test equipment chassis or case and some grounded object when the test equipment is connected to the UPS while it is operating.

Repair and Replacement Information

DANGER

Insure that all power sources are fully disconnected from the UPS before performing any repair or replacement procedures.

6.03 When a trouble symptom is localized to a faulty circuit card, that particular circuit card should be replaced in its entirety. No attempt should be made to troubleshoot or repair individual drcuit cards.

6.04 If the suspected cause of a trouble symptom is an out of adjustment condition, the particular adjustment setting should be checked or reset using the appropriate adjustment procedure.

6.05 Component failures are sometimes evident during a visual inspection of the circuitry. Obvious trouble symptoms such as loose connections, overheated, discolored, or burned components, open fuses, or burned and melted wire insulation should be corrected and the cause determined before proceeding with more detailed maintenance procedures.

6.06 Various test procedures referenced on the fault isolation chart provide the servicing technician with suggested methods for testing certain circuit components and checking for the presence or absence of certain system control signals. Whenever a numbered test procedure is encountered in the chart, service personnel should refer to that particular test procedure in this section for further troubleshooting information.

6.07 Semiconductor device outlines for diodes and thyristors (SCR's) used in this UPS are shown in Figure 6-I. The outlines are intended to aid servicing personnel in identifying semiconductor terminations during troubleshooting or replacement procedures.

Test Procedures

6.08 The following test procedures are those referenced by number on the fault By following one of the isolation chart. appropriate procedures, the servicing technician can perform a test on components suspected of failure, or on the system to verify In the operational an operating condition. test procedures, appropriate cautionary statements are inserted where necessary. The servicing technician must observe the contents of each statement and follow normal precautionary procedures necessary when servicing electronic equipment to minimize the possibility of accidental electrical shock.

6.09 Test Equipment. The following test equipment is necessary to perform the procedures in this section:

Digital Voltmeter

Fluke Model 8022A or equivalent

Oscilloscope

Tektronix Model T921 or equivalent

Insulated Screwdriver or alignment tool

CC Electronics type 8728A o r equivalent 6.10 Test Procedure 1. Perform this test when checking a capacitor for opens or shorts.

- a) Refer to paragraph 3.06, and perform system shutdown. Insure that AC and DC power sources are disconnected.
- b) Allow at least 60-seconds to elapse from time of system shutdown, then disconnect both leads or terminals of capacitor to be tested.
- c) Set the digital voltmeter (DVM) for ohmmeter function, using a high resistance range.
- d) Momentarily short the capacitor leads or terminals to insure complete discharge. Connect meter test leads to capacitor leads or terminals, and observe indicated resistance. For a good capacitor without any faults, initial resistance is low and gradually increases as capacitor takes a charge. Final resistance is usually several hundred thousand ohms, approaching I Initial high resistance approaching infinity indicates an open Initial and continued low capacitor. resistance near zero ohms indicates a shorted capacitor. Replace any capacitor which indicates an open or shorted condition.
- e) Perform Steps b) through d) on all capacitors suspected of being faulty.
- f) When all test are completed, insure that wiring to subject capacitors is properly connected.
- g) If no further tests are required, refer to paragraph 3.05 and perform system startup.
- 6.11 Test Procedure 2. Perform this test when checking a diode for opens or shorts.
 - a) Refer to paragraph 3.06 and perform system shutdown. Insure that AC and DC power sources are disconnected.

- b) Disconnect all wiring from one terminal of the diode to be tested.
- c) Set the DVM for ohmmeter function, using 1K resistance range.
- d) Connect meter test leads to diode terminals, and note indicated resistance. Reverse test lead connections, and again note indicated resistance. A diode without any indicates a low resistance, typically 0.7K ohms when forward biased, and nearly infinite resistance when reverse biased. A shorted diode typically indicates zero ohms regardless of test lead connections; conversely, an open diode indicates very high resistance regardless of test lead connections. Replace any diode which indicates an open or shorted condition.
- e) Perform Steps b) through d) on all diodes suspected of being faulty.
- When all tests are completed, insure that wiring to subject diodes is properly connected.
- g) If no further tests are required, refer to paragraph 3.07 and Perform system startup.
- 6.12 Test Procedure 3. The following procedure tests the performance of the gate driver/power supply circuit card, A2, under operating conditions.
 - a) Allow the system to remain in operation, and open the front door of the cabinet after loosening three retaining screws. Locate the gate driver/power supply circuit card, A2, mounted on the inner surface of the front door.

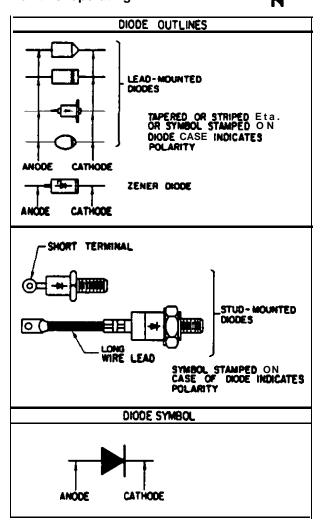
DANGER

Voltages capable of producing severe, perhaps fatal, electrical shock are present at various components throughout the UPS. When performing the steps in the balance of this test procedure, exercise extreme caution that contact is not made with any electrical termination. Insure that all test leads and probes are adequately insulated since test connections are made to electrically energized components.

- b) Check the power supply portion of circuit card A2 before proceeding with other tests.
 - 1) Set the DVM for voltmeter function. Adjust the instrument range to indicate -15 and -30 VDC.
 - Connect meter common lead to the positive (+) lead of CI or C3 on circuit card A2. Refer to Figure 6-2.
 - Check for -15 VDC at positive (+) lead of CI, and for -30 VDC at negative lead of CI. Allowable voltage tolerance of each supply is ± 10 percent.
 - 4) Connect meter test lead to test point WI, shown in Figure 6-2, the cathode lead of CRI. Voltage at WI should be -35 VDC, ± 10 percent.
 - 5) If any of the voltages checked is missing or out of tolerance, perform system shutdown procedure and replace circuit card A2.
- c) Check the gate driver portion of circuit card A2 using the following steps:

DANGER

Do not use a grounded oscilloscope when performing this procedure; otherwise, damage to the UPS may result. At the same time, a personnel shock hazard exists since the chassis and case of ungrounded, AC-operated test equipment becomes energized to the same voltage level as the circuit under test. Servicing personnel must exercise caution that simultaneous contact is not made with the test instrument chassis or case and some grounded object when the test equipment is connected to the UPS while it is operating.



- Adjust oscilloscope vertical sensitivity for 10 volts per Centimeter (10V/cm). Adjust horizontal time base for 2 milliseconds per centimeter (2 ms/cm).
- Connect common lead of probe to positive (+) lead of capacitor C2 or C3 on circuit card A2. Refer to Figure 6-2.
- 3) If checking for gate pulses to inverter SCR's CR6 and CR7, connect oscilloscope probe to test points WI and W2, respectively, shown in Figure 6-2. If checking for gate pulses to static switch

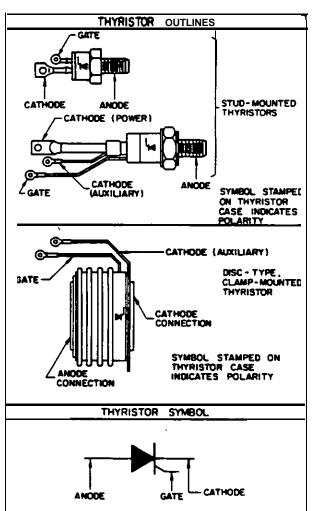


Figure 6-1. Semiconductor Oultines

SCR's CR9 and CRIO, connect oscilloscope probe to test points W4 and W 3, respectively, shown in Figure 6-2. The displayed waveform should be approximately the same as that shown in Figure 6-3, except inverted.

- 4) Any pulse group missing or distorted indicates a -fault on either circuit card AZ or AI. Replace A2 first; if the trouble symptom is not cleared, then replace control circuit card A 1.
- 5) If all pulse groups are present at WI through W4 on circuit card A2 but the trouble symptom persists, check for the SCR gating waveform at the gate and cathode terminals of CR6, CR7 or CR9, CRIO. If this test is performed, connect the probe common lead to the cathode (K) terminal of the device under test. Connect the oscilloscope probe to the gate lead of each SCR being checked.
- 6) The gating waveform displayed should be the same as that shown in Figure 6-3. If any waveform is missing, replace circuit card A 2.

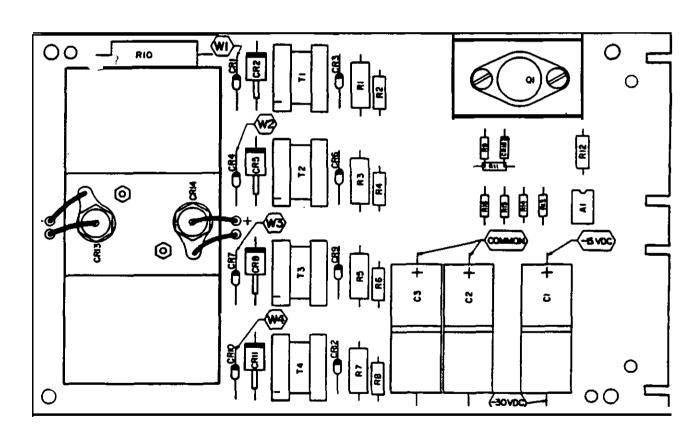
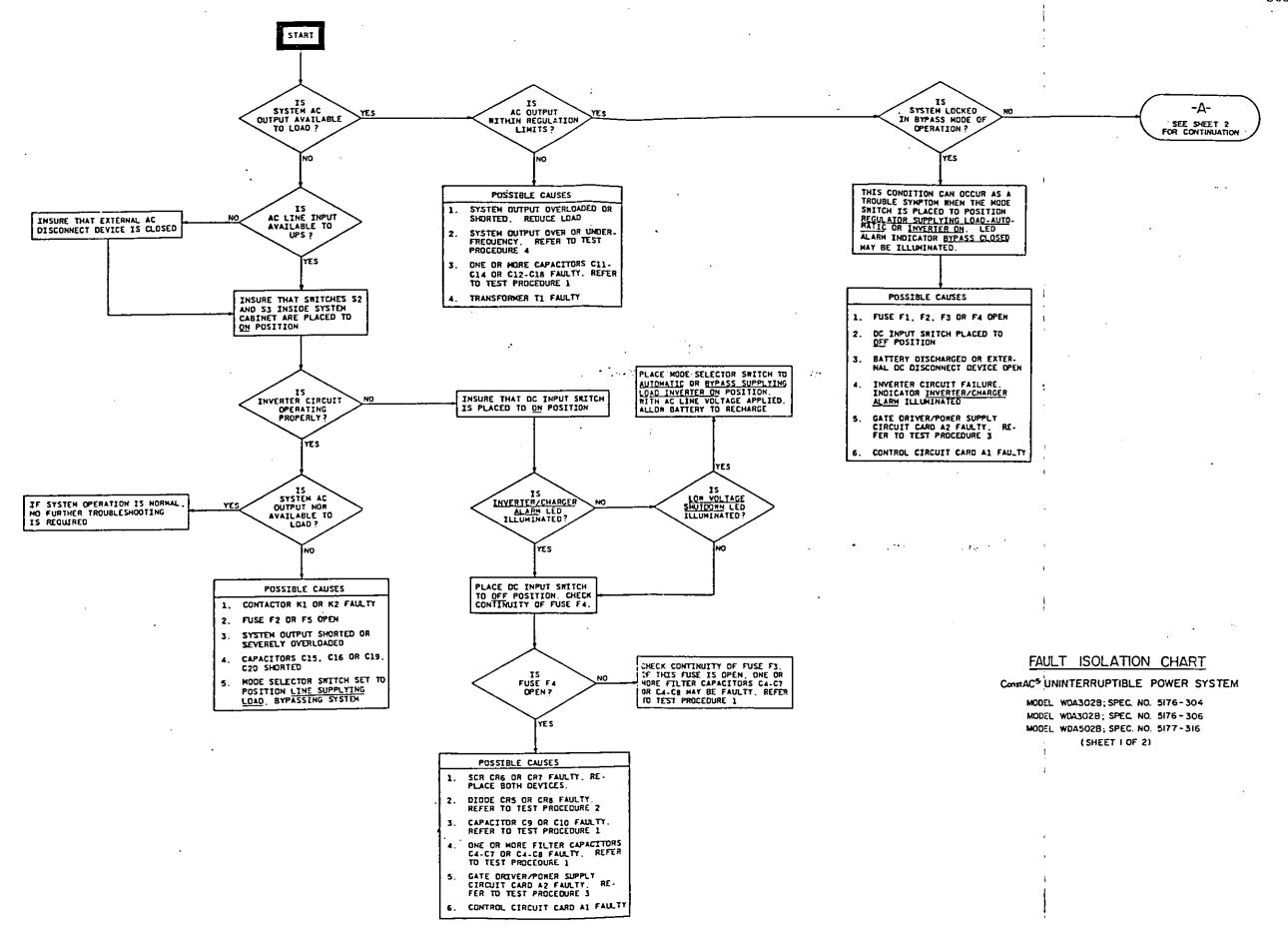
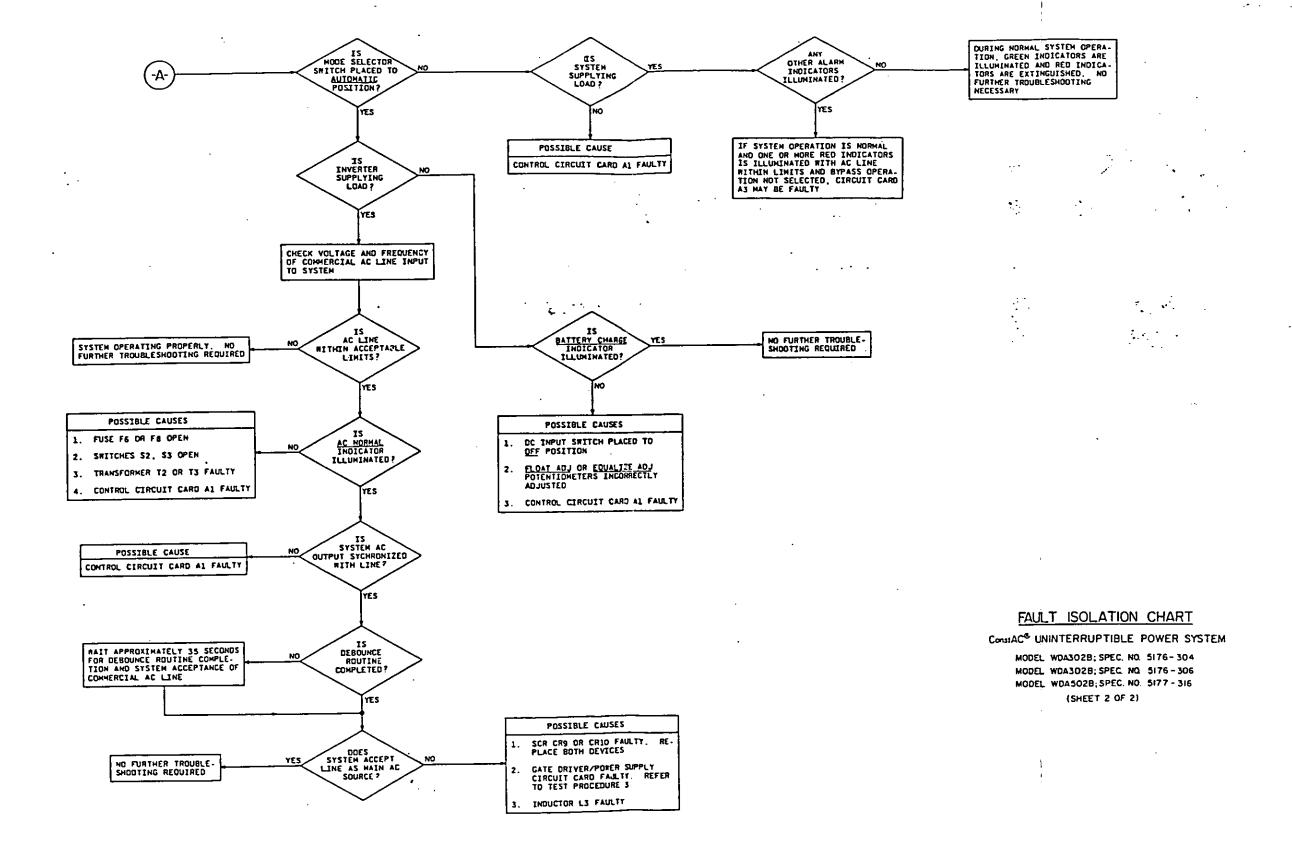


Figure 6-2. Gate Driver/Power Supply Circuit Card





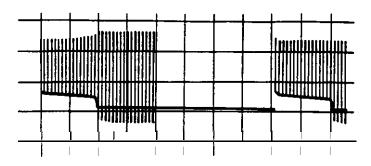


Figure 6-3. Gating Pulse Waveform

- 6.13 <u>Test Procedure 4.</u> Observe the following procedure when checking and adjusting inverter frequency.
 - a) If UPS is already operating, place mode selector switch to BYPASS SUP-PLYING LOAD-INVERTER ON position.
 - Release three retaining screws and open front door of cabinet. Place switch \$3 to the OFF position.

DANGER

Voltages capable of producing severe, perhaps fatal, electrical' shock are present at various components throughout the UPS. When performing the steps in the balance of this test procedure, exercise extreme caution that contact is not made with any electrical termination. Insure that all the leads and probes are adequately insulated since test connections are made electrically to energized components.

c) Adjust oscilloscope to monitor a 60 Hz sine wave of approximately 55 volts peak-to-peak amplitude. The triggering source for the oscilloscope should be the line (60 Hz AC) or an external, stable 60 Hz source. d) Insure that the oscilloscope is not grounded through the power cord if an AC-operated instrument is being used. Connect the probe common lead to circuit card AI test point TP5, Connect the oscilloscope probe to test point TP2. Refer to Figure 6-4 for test point locations.

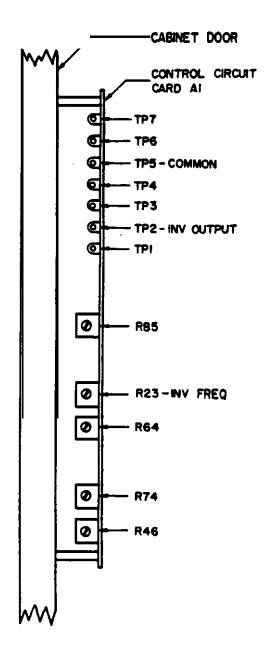


Figure 6-4. Potentiometer and Test Point Locations

- e) Observe the waveshape on the oscilloscope. If the line is used as, the triggering source, the waveshape should be stationary on the CRT. If any other type of triggering is used, measure the period of one wavelength. It should be 16.66 milliseconds at 60 HZ.
- f) If inverter output frequency is other than 60 Hz exactly, adjust potentiometer R23 (INV FREQ) until correct output frequency is attained.

WARNING

Do not attempt adjustment of any other potentiometers on circuit card Al. Only R23 should be adjusted.

- g) After adjustment is completed, place switch \$3 to the ON position. Place mode selector switch to AUTOMATIC position.
- h) Wait approximately 35 seconds for completion of system debounce routine. System should accept line as main AC source.
- i) Check system operation. No red alarm indicators should be illuminated at this time. Check system output under load and at no load for proper voltage, nominally 117 VAC at unity power factor.
- j) After all tests are completed, disconnect oscilloscope probe. System may be returned to service if operation is normal.

APPENDIX

RECORD OF INSTRUCTION CHANGES				
ISSUE OR DATE	ECN	DESCRIPTION OF CHANCE		
2	- ,	Spec. No. 5176-306 added.		
3	183-8172	Circuit card A I changed from a 4864-221 to a 4864-4 19. The accessory meter panel is now standard.		
		CONVERSION TO LAMPS		
DI	183-8907	Circuit card Al changed from 4864-419-00 to 4864-732-00. Remove status panel deleted.		
EI	183-8783	Slow Charge Circuit		
F1	_	Reworded Paragraph 2.03 for clarity.		
G 1		Low Voltage Shutdown Adjustment Procedure added.		

LORAIN PRODUCTS

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In **Canada** — Reliance Telecommunication Products Ltd., 122 Edward St., St. Thomas. Ont. TEL. 519-631-0780/TWX 610-356-6703 (LORAIN PRODCAN)

In **Mexico** — Productos Lorain de Mexico. S.A. de C.V.. Apartado Postal 77001 TEL. 576-82-77/TLX /01774308 (1774308 LORME)



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	 t ,	

- REPLACEMENT OF DISC-TYPE THYRISTORS AND DIODES USING CLAMP ASSEMBLY 2868-234-00 OR 2868-235-00

CONTENTS	PAGE	Quantity	Description
1. GENERAL	1	I Set	Socket wrenches with ratchet handle and exten-
2. REQUIRED TOOLS AND MATERIALS	1		sion; 3/8 or 1/2 inch drive
3. INSTALLATION PROCEDURE	1	_	
APPENDIX		ı	Screwdriver; medium sized flate blade
1. GENERAL		1	Force gauge; L.P. Part No. 6774-007-00
1.01 These instructions provide a step procedure for replacement of thyristors or diodes. These semicare used in various types LORAIN® and the procedures outlined in thes tions should be followed to correct disc-type thyristors or diodes.	disc-type conductors quipment, e instruc-	I Container	Thermal joint compound; Wakefield Engineering, Inc., Type 120 (L.P. Part No. 1762-510-00), o requivalent

- 1.02 Refer to Figure I for identification outlines of the disc-type thyristor and diode. The replacement device should be compared to the outline drawing prior to replacement. Identify the polarity and type of the replacement semiconductor to insure that the new diode or thyristor is correctly installed in its heatsink.
- 1.03 Figure 2 provides an illustration showing force gauge use during the replacement procedure. Refer to Figures 2A through 2C during installation to insure that correct mounting force is exerted on the replacement diode or thyristor.

2. REQUIRED TOOLS AND MATERIALS

2.01 The following tools and materials are required to replace a "disc-type" semi-conductor:

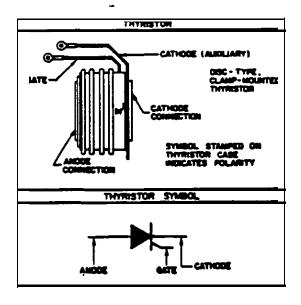
3. INSTALLATION PROCEDURE

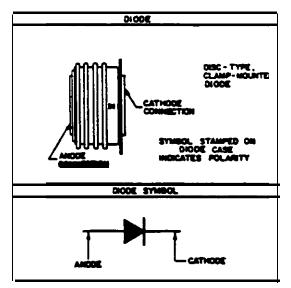
3.01 Remove all sources of power to the equipment requiring repair. Open all AC and/or DC input and output circuit breakers or switches according to the Stopping Procedure in the appropriate instruction manual. Open the external disconnect on protective device prior to the replacement procedure.

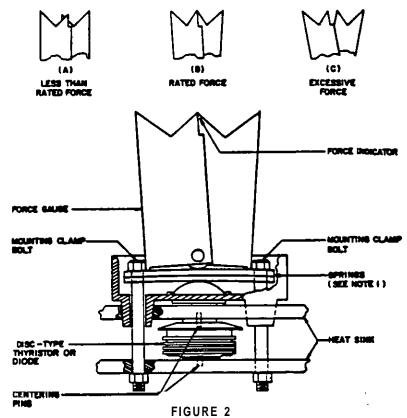
NOTE

Refer to the Schematic Diagram of the equipment being repaired. Insure that the equipment is completely isolated from all power sources. In some instances, it may be necessary to remove input, output, and control fuses to completely isolate the equipment from any power source.

FIGURE I SEMICONDUCTOR IDENTIFICATION OUTLINES







CUT-AWAY VIEW OF "DISC-TYPE" SEMICONDUCTOR INSTALLATION DETAILS

NOTE 1: CLAMP ASSEMBLY (L.P.C. PART NO. 2868-234) HAS TWO SPRINGS. CLAMP ASSEMBLY (L.P.C. PART NO. 2868-235) HAS FOUR SPRINGS.

DANCER

Hazardous voltage may be present within the cabinets of some LORAIN equipment after input and output power sources have been disconnected. Servicing personnel must exercise extreme caution that contact is not made with interconnection terminal blocks or any portion of the circuitry other than the diode or thyristor assembly requiring repair.

3.02 Open the equipment access doors or covers and remove any inner protective grilles or covers to gain access to the diode or thyristor assembly. Proceed as follows:

- a) If a thyristor is being replaced, disconnect the cathode (K) and gate (G) leads from their terminal board. These leads are usually red and white wires, and are connected to a terminal board mounted on the heatsink near the thyristor.
- b) Loosen and remove the two mounting clamp bolts. The clamp, heatsink, and semiconductor may now be removed from the equipment.

NOTE

NOTE THE POLARITY OF THE FAULTY SEMICONDUCTOR BEFORE REMOVING IT FROM ITS HEATSINK.

c) One mounting surface is the anode whereas the other connection, mounting surface is the cathode connection. The polarity of the device is usually indicated by a symbol stamped on the ceramic portion of the device. Refer to the Identification Outlines (Figure 1) if any doubt exists concerning the polarity of the device. It may be necessary to remove circuit wiring or loosen a busbar connection from the heatsink before it can be removed from the equipment.

- d) Remove the faulty semiconductor from its heatsink. Using a clean rag or paper towel clean both portions of the heatsink assembly to remove any thermal compound residue.
- e) If a thyristor is being replaced, carefully inspect the gate lead connection at the pin entering the ceramic body of the thyristor. It is of utmost importance that this connection is tight. If this connection point is suspected of being loose, carefully solder the push-on-connector to the pin to insure good electrical contact.
- f) Apply a thin coating of thermal joint compound to the mounting surfaces of the replacement semiconductor.

WARNING

Do not apply an excessive amount of thermal joint compound on the mounting surfaces of the replacement device. The compound possesses electrical insulating qualities, and may prevent the semiconductor device from functioning properly if an excessive amount is applied. A thin, almost transparent coating is adequate.

- g) Install the replacement device on the heatsink, observing correct polarity and insuring it is correctly centered on the pins imbedded in the heatsinks. Replace the mounting clamp bolts, and secure them finger-tight only.
- h) Using a force gauge (L.P. Part No. 6774-007-00) placed against the clamp springs as shown in Figure 2, tighten the mounting clamp bolts alternately, 1/4 turn at a time. The bolts should be tightened equally until the force gauge indicates rated force is applied as shown in Figure 2 (B).

NOTE

If excessive force is applied by OVETtightening the tinting clamp bolts,
as shown by the force gauge indication
in Figure 2 (C), completely loosen the
two bolts and repeat the tightening
process. Never attempt to adjust
spring tension by backing off the bolts
since the force gauge may provide a
false indication in this case.

- i) When the mounting clamp bolts are 'correctly tightened, remove the force gauge. Reconnect any wiring removed from the heatsinks, insure that all hardware used to make electrical connections is tightened securely.
- j) If replacing a thyristor, reconnect gate and cathode leads to their respective terminals. Refer to the equipment Wiring Diagram (T-drawing) for details.

WARNING

insure that these leads are connected to the proper terminal board connection points, or damage to the equipment and semiconductor device may result when power is applied.

NOTE

On the gate and cathode lead terminal board, the cathode connection is usually marked "K" and the gate connection is usually marked "G". If any doubt exists regarding the correct connection points, refer to the equipment wiring diagram.

- 3.03 After all electrical connections have been made, replace any protective grilles or covers which were previously removed. Close the equipment access doors or covers.
- 3.04 Restore input power to the equipment, and insure that any output disconnect or protective devices are connected or reset for normal operation. If necessary, refer to the Starting Procedure in the appropriate instruction manual to restore the equipment to normal operation.

APPENDIX

ſ	RECORD OF INSTRUCTION CHANGES				
ISSUE OR DATE	ECN	DESCRIPTION OF CHANGE			
DI	183-7444	Revised to reflect current instruction format.			

LORAIN PRODUCTS

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in Canada —

Reliance Telecommunication Products Ltd., 122 Edward St., St. Thomas. Ont. TEL. 519-631-0780/TWX 610-356-6703 (LORAIN PRODCAN)

in Mexico —

Productos Lorain de Mexico. S.A. de C.V.. Apartado Postal 77001 TEL. 576-82-77/TLX./01774308 (1774308 LORME)

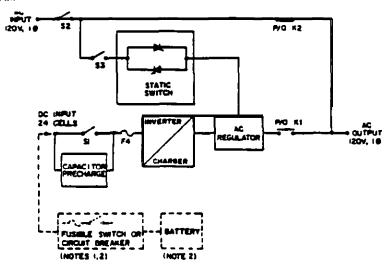


1.1 GENERAL: The LORAIN® ConstAC® Uninterruptible Power System (UPS) provides single phase AC power to the loat The system includes an inverter, battery charging capability. AC disconnect means, and an AC voltage regulate contained in a common capinet. This UPS is listed by Underwriters Laboratories Inc. under Standard 1012 for power conversion equipment in a controlled environment.

1.2 MODES OF OPERATION

- 1.2.1 Normals: When the commercial AC line voltage and frequency are within acceptable limits, the UPS is a batter charger and line filter. Commercial AC line is applied through the static switch to the AC regulator/filter whic supplies filtered regulated AC to the load. The inverter/charger maintains (floats) or recharges an externa customer furnished battery.
- 1.2.2 Line Failure: During line disruptions such as blackouts, brownouts, interrupts, transients, or frequency variations, the static switch disconnects the commercial AC line allowing the inverter, which derives power from the batteries, to continue to supply filtered regulated AC power to the load. No break in load power occurs upole failure of the AC line. When the line disruption ends, the UPS monitors the commercial AC line to approximately 30 seconds, to insure that the line is stable, then returns to normal operation.
- 1.2.3 Bypass: Load power is obtained directly from the commercial AC line, bypassing the inverter and regulate circuits. Load power is not interrupted during transfer.

1.3 ONE LINE DIAGRAM



NOTES

- I. A fusible switch or circuit breaker, provided by the customer, should be connected between the battery and DC input terminals of the system. The device should be rated for an interrupting capacity in amperes at least It times the ampere-hour capacity of the battery. Refer to the Installer's Information Notes for required amperaging ratings of the protective device. A slow acting fuse or circuit breaker capable of withstanding momentary surgicularity is necessary.
- 2. Dotted lines indicate equipment and wiring not furnished with the system.

PD5177-3 LORAIN PRODUCTS LORANL OHIO RELIANCE COMM/TEC **POWER DATA** ConstAC® UNINTERRUPTIBLE POWER SYSTE MODEL WOA3028, SPEC. NO. 5176 - 304-1 MOV 02 1987 MODEL WDA5028, SPEC. NO. 5177-316 -PARAGRAPH 4.1.2 CORRECTED P١ RELAY CONTACT RATING ON THE TO CHANGED DE MIZ-90 DEC 11 1988 N1 183-9588 PD5176-304 146 NO DESCRIPTION 53 U.E 13.7.81 AGE REVISIONS

1.4 OUTPUT RATINGS

1.4.1 System AC Output:

- (A) Capacity: 3000 volt-amperes for Model WDA302B; 5000 volt-amperes for Model WDA502B.
- (B) Voltage: 120 voits. 60 Hz, single phase.
- (C) Regulations: Output voltage remains within the range of 110 to 126 volts over specified AC and DC input ranges from no load to full load at 0.3 power factor leading or lagging.
- (D) Harmonic Distortion: The total harmonic distortion of system output sine wave does not exceed 5% from no load to full load.

(E) Frequency Stability:

- (1) System output is synchronized to commercial AC line when line frequency is within the range of 60 ± 0.9 Hz.
- (2) If commercial AC line fails or frequency exceeds specified limits, the free-running inverter frequency remains within the range of 60 ± 0.3 Hz.

1.4.2 Battery Charger Output:

- (A) Voltage: Adjustable from 49.9 to 58.75 VDC for charging a 23 or 24 cell battery. A FLOAT/EQUALIZE switch on the system front panel allows the manual selection of either the float or equalize output voltage. Separate potentiometers allow independent adjustment of the float and equalize output voltages.
- (B) Regulations Steady state DC output voltage remains within ± 0.5% of the adjusted voltage over the AC input range and from no load to full load.
- (C) Current: 18 amperes for Model WDA302B: 30 amperes for Model WDA502B.

1.5 INPUT RATINGS

1.5.1 System AC Input:

- (A) Voltage: 100 to 127 volts with output power factor of 0.3 leading or lagging. Refer to Paragraph 1.19.1.
- (B) Frequencys AC input frequency applied to the system must remain within the limits of 60 ±0.9 Hz. Refer to Paragraph 1-10.1 for additional information.

(C) Typical Input Datas

MODEL WDA302B

AC Input Voltage	Load	Amperes	Watts	Battery Charging Current DC Amperes	Efficiency %
100	50% 100%	11.0 20.7 36.2	443 1933 3533	0	0 77.6 8+.9
117	0 50% 100%	5.5 17.2 31.0	453 1983 3453	0	0 75.6 36.9
127	0 50% 100%	5.0 16.5 29.3	473 2033 3533	0 0 0	0 73.8 84.9

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1.5.1 (C) Typical Input Data (CONT'D)

MODEL WDA 502B

AC Input Voltage	Load	Amperes	Watts	Battery Charging Current DC Amperes	Efficiency
100	0 50% 100%	15.4 32.2 60.6	710 3070 5980	0	0 76.2 34.4
117	0 SO% 100%	6.7 26.6 11.6	735 1120 6060	0 0 9	0 75.6 36.3
127	2 100%	7. \$ \$1.0	770 6080	9 0 0	0 75.1 36.3

(D) Maximum Input Current:

- (1) Model WDA3028:
 - (a) 40.0 amperes: output loaded to 3 KVA at 117 VAC with 18 ampere battery charging current.
 - (b) Does not exceed 45.7 amperes at 100 VAC input with the system output overloaded while charging battery at 18 amperes.
- (2) Model WDA502B:
 - (a) 64 amperes with the output loaded to 5 KVA at 117 VAC with 30 ampere battery charging current.
 - (b) Does not exceed 74.9 amperes at 100 VAC input with the system output overloaded while charging battery at 30 amperes.

1.5.2 System DC Input:

(A) Model WDA302B:

- (1) Voltage: 42 to 56.4 volts.
- (2) Current: Does not exceed \$5.1 amperes at 42 volts.
- (3) Typical Input Data:

	Inverter		
System Load	DC Amperes at 1.75 Volts/Cell		
No Load	10.8		
Haif Load 59.2 Full Load 85.1			

	Inverter		
point DC	Efficiency %		
42 Valts	34.2		
48 Voits	34.5		
52 Volts	\$3.6		

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PAGE

1.5.2 System DC input (CONTD)

(B) Model WDA502B:

- (1) Voltage: 42 to 56.4 volts.
- (2) Current: Poes not exceed 147 amperes at 42 voits.
- (3) Typical Input Data:

Inverter		
System DC Amperes Load at 1.75 Voits/Ce		
No Load	17.0	
Haif Load	74.5	
Full Load	146.5	

	Inverter
DC	Efficiency %
42 Volts	82.0
48 Voits	34.0
52 Volts	\$5.3

1.6 STANDARD FEATURES

1.6.1 DC Input Protections

(A) Precharge Circuit: These UPS systems are equipped with a precharge circuit which charges the DC capacitors prior to starting the UPS.

(B) Model WDA302B:

- (1) Doubte pote 225 ampere disconnect opens both DC input teads.
- (2) 150 ampere fuse provides instantaneous overload protection for inverter thyristors.

(C) Model WDA502B:

- (1) Double pole 225 ampere disconnect opens both DC input leads.
- (2) 300 ampere fuse provides instantaneous overload protection for inverter thyristors.
- 1.6-2 Output Protection: Inherent magnetic characteristics of ferroresonant transformer protects system against excessive overloads and short circuits. Instantaneous short circuit conditions may result in operation of an inverter fuse.
- 1.6.3 Metering: The UPS is equipped with four meters which monitor battery voltage, charge/discharge current, load voltage and load current. Each meter is a 2.5 inch rectangular type, with a 2.41 inch scale length, at ±2% accuracy.

(A) Model VDA302B:

DC Voltmeter: 0-75 volts DC

DC Ammeter: 150-0-150 amperes DC

AC Voltmeter: 0-150 volts AC
AC Ammeter: 0-50 amperes AC

(B) Model WDA502B:

DC Voltmeter: 0-75 volts DC

DC Ammeter: 200-0-200 amperes DC

AC Voltmeter: 0-150 volts AC AC Ammeter: 0-75 amperes AC

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- 1.6.4 Indicators: The following indicators are located on the system front panel.
 - (A) BYPASS CLOSED: A red indicator illuminates whenever load power is supplied through the bypass circuit rather than the system AC regulator. Simultaneously, an open circuit is present at TB4, terminals 3 and 4, and a closed circuit is present at TB4, terminals 4 and 5.
 - (B) LOT VOLTAGE SHUTDOWN: A red indicator illuminates when automatic inverter shutdown results from low battery voltage.
 - (C) INVERTER/CHARGER ALARM: A red indicator illuminates if a fault occurs in the inverter/battery charger circuit which results in an open fuse.
 - (D) POWER ON: A green indicator illuminates whenever DC input is applied to the system and AC output power is supplied to the load.
 - (E) CHARGE: A green indicator illuminates and an open circuit is available at TB4, terminals 1 and 2 whenever the system supplies charging current to the battery.
 - (F) DISCHARGE: A red indicator illuminates and a closed circuit is available at TB4, terminals 1 and 2 whenever the battery supplies input power to the inverter.
 - (G) REGULATOR ON: A green indicator illuminates when the system supplies load power through the AC regulator, using either the commercial AC line or inverter as the source.
 - (H) AC LINE NORMAL: A green indicator illuminates whenever commercial AC power applied to the system is within the acceptable voltage and frequency limits.
 - (I) STATIC SWITCH CLOSED: A green indicator illuminates whenever the static switch is enabled, supplying commercial AC power to the load through the AC regulator circuit. A closed circuit is available at TB4, terminals 3 and 4, and an open circuit is present at TB4, terminals 4 and 5.
 - (J) DS1: Illuminates to indicate the state of charge on the DC filter capacitors during the startup procedure.
- 1.6.5 Selector Switch: A four position rotary switch on the system front panel allows manual selection of system operational modes. The function of each position is described as follows:
 - (A) REGULATOR SUPPLYING LOAD:
 - (1) AUTOMATIC: Commercial AC power supplies the load through the static switch and AC regulator, and charges the battery when voltage and frequency are within acceptable limits. If AC line fails in this mode, inverter automatically supplies load with no break in output power.
 - (2) INVERTER ON: Inverter operates from battery power and supplies AC to load, regardless of commercial AC line status. Inverter failure results in automatic transfer to bypass circuits.
 - (8) BYPASS SUPPLYING LOAD:
 - INVERTER ON: Commercial AC power supplies load through bypass circuit while system charges the battery.
 - (2) INVERTER OFF: Commercial AC power supplies load through bypass circuit. Inverter/charger circuit is disabled.

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1.7 ENVIRONMENTAL RATINGS

- 1.7.1 Operating Temperature Range: 0 ° C to 50 ° C (32 ° F to 122 ° F)
- 1.7.2 Storage Temperature Range: -40 ° C to \$5 ° C (-40 ° F to 185 ° F)
- 1.7.3 Heat Dissipation: 127 VAC input with system output fully loaded:
 - (A) Model WDA302B: 1545 Btu/hr
 - (B) Model WDA502B: 2796 8tu/hr
- 1.7.4 Ventilation Requirements: Ventilation openings in cabinet must be unobstructed and the temperature of the air entering the cabinet must not exceed 50 ° C (122 ° F).
- 1.7.5 Audible Noise: The audible noise at any point five feet from any vertical surface of the UPS does not exceed 67DB "A" weighting when measured with a General Radio 1551C sound level meter (or equivalent) conforming to ANSI S1.4. Refer to Paragraph 1.10.2.

I.S ACCESSORIES

1.3.1 Rear Dress Panels Part No. 3517-556-00; covers protruding hardware from recessed rear panel of system.

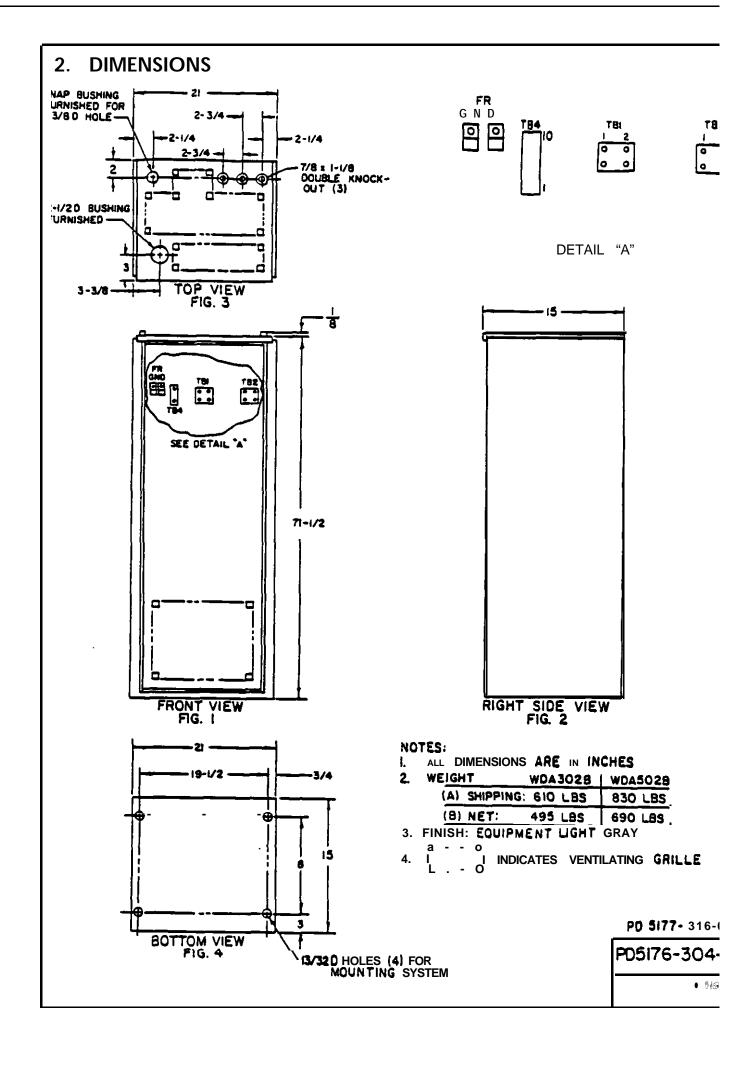
1.9 RELATED DOCUMENTATION

- 1.9.1 Schematic Diagram:
 - (A) Model WDA302B: SD-5176-304-00
 - (B) Model WDA502B: 5D-5177-316-00
- 1.9.2 Wiring Diagram:
 - (A) Model WDA302B: T-5176-304-00
 - (B) Model WDA502B: T-5177-316-00
- 1.9.3 Instructions: Section 4204

1.10 SPECIFICATION NOTES

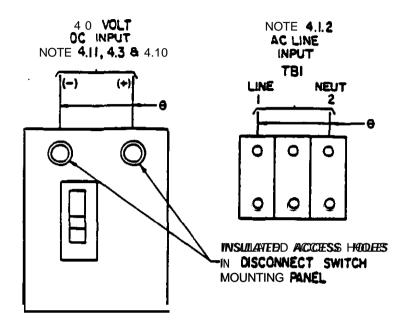
- 1.10.1 If AC input voltage and/or frequency variations exceed the specified limits, this UPS automatically disconnects from the input line and operates from battery power. If a particular application of this UPS requires operation from an AC source that may be expected to exceed specified frequency and voltage limits, such as an engine alternator, consult the factory.
- 1.10.2 This parameter is guaranteed by engineering design and test, and is not production tested.

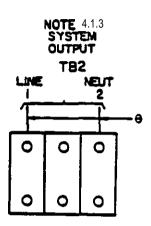
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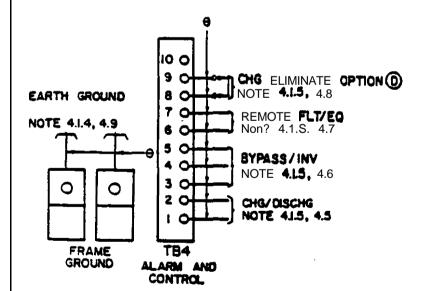


3. INSTALLER'S CONNECTIONS

3.1 MODEL WDA302B



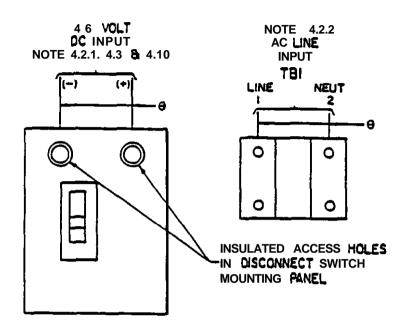


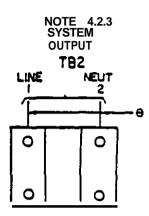


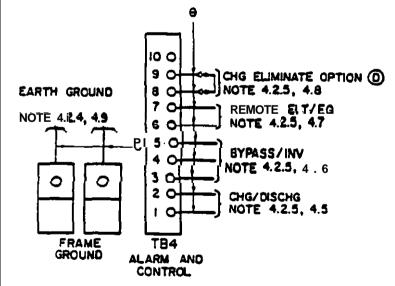
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3.2 MOOEL WDA502B







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4. INSTALLER'S INFORMATION NOTES

4.1 MODEL WDA3028

4.1.1

	DC INPUT TO DISCONNECT SWITCH SI					
Ter	rminais	Loop Length	Recm Wire	Recm		
Capacity	Туре	Ft. Note 4.3	Size	Fusing		
	5/16-inch	33_	l Ga.			
6 Ga. to	Allen socket	112	1/0 Ga.	125		
3/0 Ga.	screw	140	2/0 Ga.	am£.		

4.1.2

AC LINE INPUT (TB1)				
Te	rminals	Recm	Recm	
Capacity Type		Wire Size	Fusing	
13-2 Ga.	Saddle Clamp	6 Ca.	60 Amp	

4.1.3

SYSTEM OUTPUT (TB2)				
Terminals Recm				
Capacity	Туре	Wire Size		
15-2 Ga.	Saddle Clamp	\$ Ga.		

4.1.4

FRAME GROUND (FR CNO,				
Ter	Recm			
Capacity	Wire Size			
1/0-14 Ga.	Saddle Clamp	6 Ga.		

4.1.5

ALARM	AND CONTROL	(TB4)
Term	Recm	
Capacity Type		Wire Sire
le Ga. Max. I	5-32 Screw	16 Ga.

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4. INSTALLER'S INFORMATION NOTES

4.2 MODEL WDA502B

4.2.1

	DC INPUT TO	DISCONNECT S	WITCH SI	
Terminais		Loop Length	Recm Wire	Recm
Capacity	Туре	Ft. Note 4.3	Size	Fusing
6 Ga. to 3/0 Ga.	5/16-inch Allen socket screw	100	3/0 Ga.	200 Amn

4.2.2

AC LINE INPUT (TBI)			
Terminais		Recm	Recm
Capacity	Туре	Wire Size	Fusing
2-2/0 Ça.	Saddle Clamp	4 Ga.	30 Amp

r.2.3

SYSTEM OUTPUT (TB2)			
Те	Recm		
Capacity	Туре	Wire Size	
4-2/0 Ga.	Saddle Clamp	4 Ga.	

4.2.4

FRAME GROUND (FR GND)			
Te	Recm		
Capacity	Size	Wire Sire	
1/0-14 Ga. 1	Saddle Clamp	6 Ga.	

4.2.5

ALAR	M AND CONTROL	∟ (TB4)
Ter	Recm	
Capacity	Type	Wire Size
I6 Ga. Max.	6-32 Screw	16 Ga.

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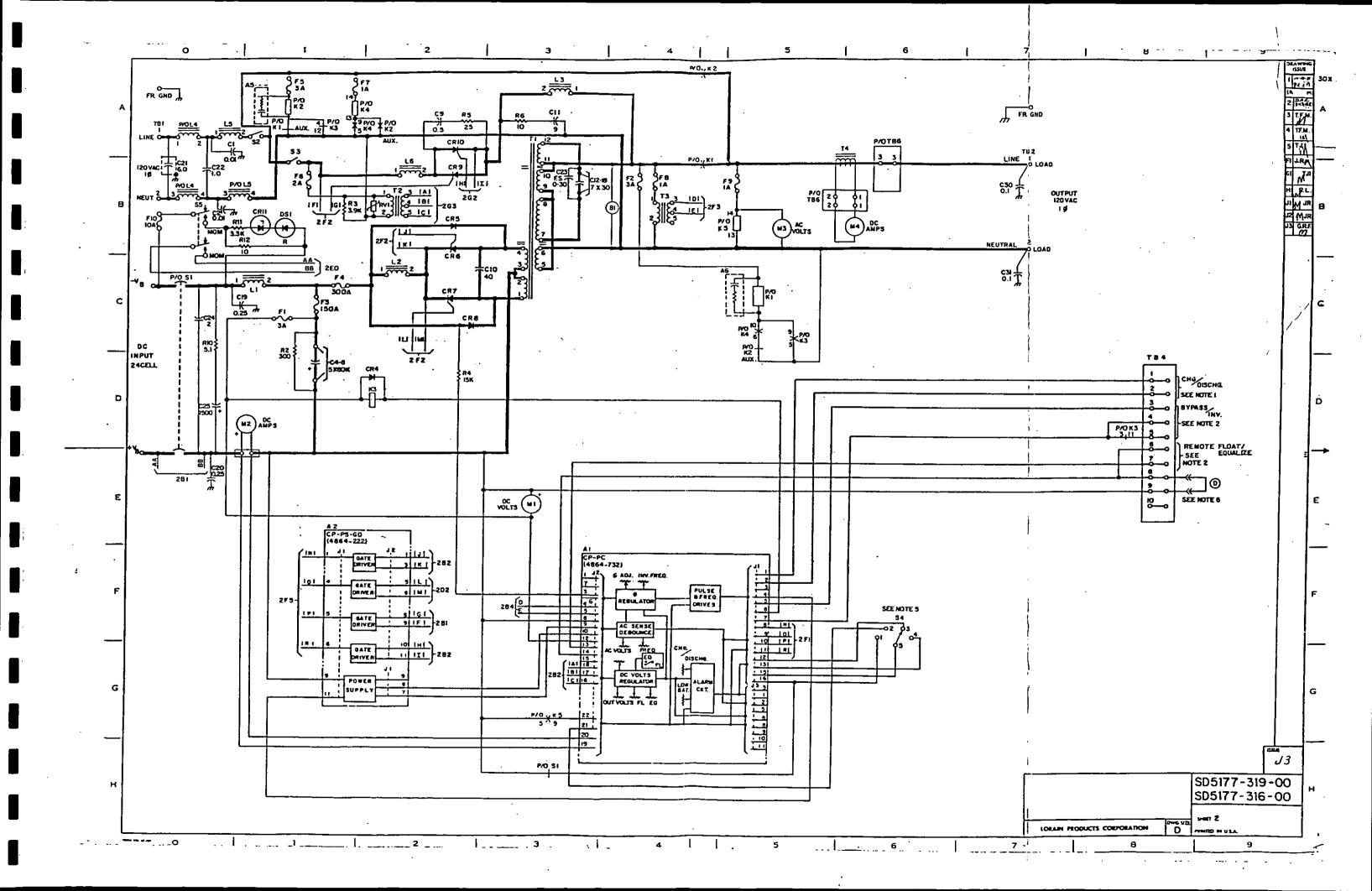
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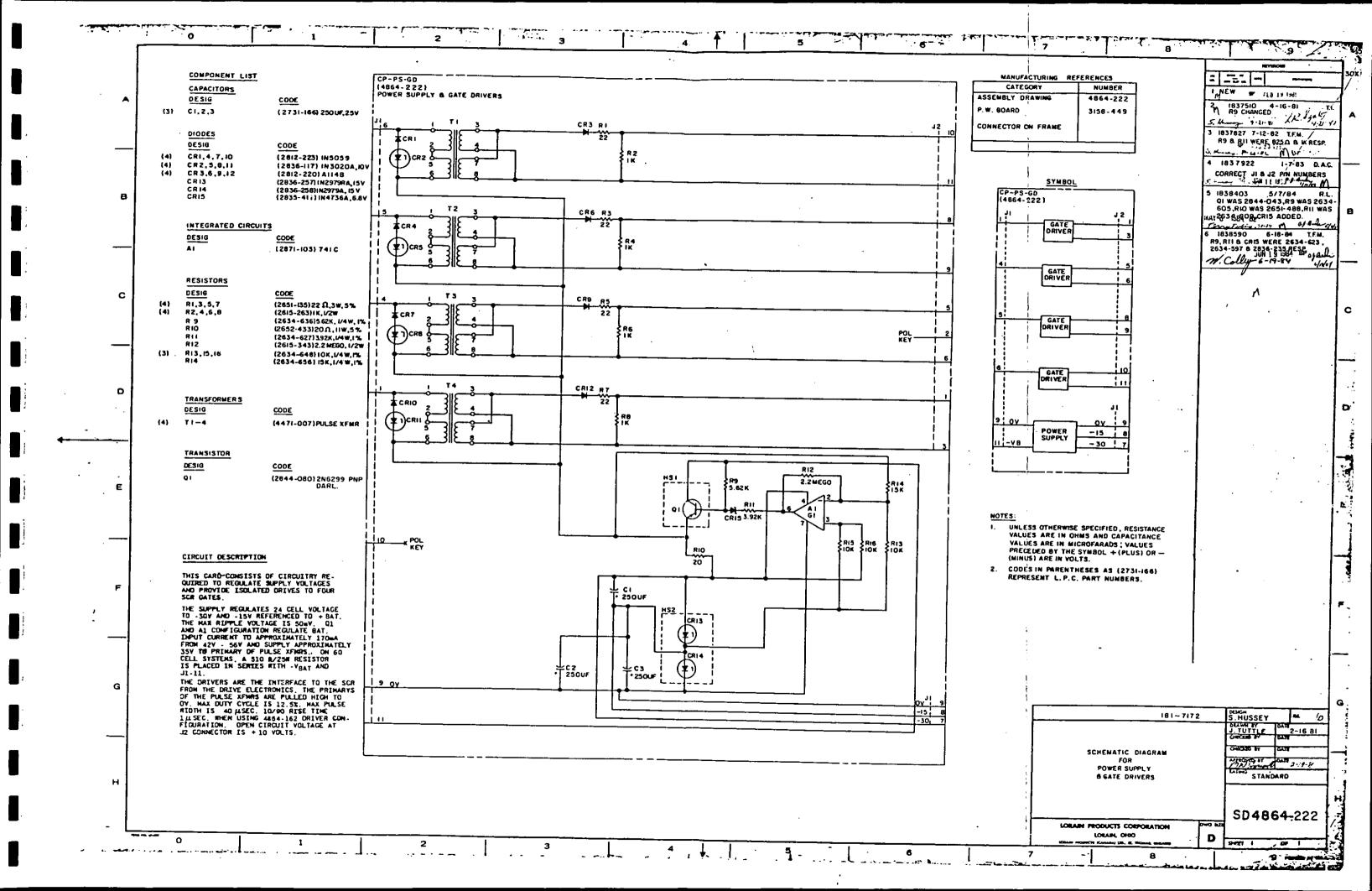
4. INSTALLER'S INFORMATION NOTES

- 6.3 DC input wiring is sufficient to restrict voltage drop to one volt or less at rated current for loop lengths shown. Loop length is the sum of positive and negative lead lengths.
- 4.4 Wire sizes based on recommendations of the National Electrical Code Table 310-16 for copper wire at 75 °C conductor temperature, operating in an ambient of 30 °C. For operation in ambient higher than 30 °C, apply derating factors listed in Table 310-16 of the National Electrical Code.
- 4.5 Terminals 1 and 2 of TB4 are connected internally to a relay contact, rated at 1.0 amp, 30 VDC. An open circuit is present when the system is charging the battery, and a closed circuit is present while battery is discharging to supply inverter input power.
- 4.6 Terminals 3 through 5 of TB4 are connected internally to relay contacts rated at 1.0 amp. 30 VDC. When system bypass circuits are activated, an open circuit is present at terminals 3 and 4, and a closed circuit is present at terminals 4 and 5. When system output supplies the load, using either the AC line or inverter as the primary source, a closed circuit is present at terminals 3 and 4, and an open circuit is present at terminals 4 and 5.
- 4.7 An external SPST switch connected to terminals 6 and 7 of TB4 can be used for remote control of battery float and equalize function. With FLOAT/EQUALIZE switch on system front panel set to EQUALIZE position, the battery may be charged at equalize voltage by providing an open circuit at TB4, terminals 5 and 6. Closing the circuit at these terminals provides float voltage to the battery.
- 4.5 Connect a jumper to TB4 reminals 3 and 9 to disable the battery charging feature of the system. Refer to instruction form for complete description and adjustment details.
- 4.9 This terminal must be connected to earth ground, not power system neutral. Equipment grounding conductor size based on recommendations of the National Electrical Code Table 250-95 for copper wire. If aluminum or copper clad aluminum grounding conductor is used, refer to Table 250-95 for increased conductor size.
- 4.10 For proper system operation, the positive terminal of the external battery must be grounded in compliance with National Electrical Code Article 250 and/or local codes or practices.
- 4-11 Q-indicates wiring furnished and connected by customer.

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*AGE 12			

٥ 2 3 PARTS LIST QUANTITY PER MODEL 1 NEW - IN 25 1392 M A LP. LEVEL 2-15-82 D.A.C. M DESCRIPTION DESIG. METER ASSY, PART, NO. UPDATED 5-13-82 D.A.C. CIZ-CIB WAS 2714-766 11 198 O.GIUF, 250V AC 6.OUF, 660V AC 6000UF, 75V DC 0.5UF, 660V AC 40UF, 400V DC 9UF, 570V AC 1UF, 250V AC 0.25UF, 600V DC CI, C2 C21 C4-8 C9 3 1837886 10-9-82 T.F.M. M C24 ADDED, DESIG RV I WAS MEV 2716-362 2714-578 2714-767 2714-539 C11 C12-18 1837653 4-15-83 T.F.M. C22 C19,C20 C23 C24 1 1837933 4-13-03 1; m.
C21,22,23,25,14,R10,M1,2,38 4
A00ED; C2, CR2,3 8 OPTION B REMOVED; T1 WAS 4561-011, T4 WAS
C1,NOTE 4 RESERVED.
RM 4-1981 YA 64 844 0.25UF, 600V DC 0-30UF, 660V AC 2UF, 100V DC 1.5KUF, 75V DC 0.IUF, 250V AC 2715-257 FACT. SEL 2713-198 2731-235 2714-513 C25 C30,C31 15 1838172 9-14-83 TJI M WAS 4864-221; R7,8 REMOVED 10 OCT 5 1983 BP 2 November 1974-1975 Of Children CR4 3001G 2812-310 CR5.8 CR6.CR7 CR9.10 DIODE THYRISTOR 2814-238 2863-255 NOTES: WHEN THE BATTERY IS BEING CHARGED, TERMINALS 1 & 2 OF TB4 WILL BE OPEN, WHEN BATTERY IS BEING DISCHARGED, 1 & 2 OF TB4 WILL BE SHORTED, THIS CIRCUIT CAN WITHSTAND 120VAC OR 28V DC & CONDUCT 0.5 AMPS MAXIMUM. 2864-127. 2834-414 THYRISTOR CONVERSION TO LAMPS CRII ZENER 3.6V 5W 1838903 3-11-85 J.R. F1.2.5 34.250V AGC-3 CHGD K4 COIL WIRING. 2483-233 C MAR 82 1905 BP 4/m/s 150 AHP ,250V ,NON150 300 AHP ,500V 2483-541 2485-427 WHEN THE CONSTACT SYSTEM IS IN BYPASS MODE, TERMINALS 3 & 4 DF 184 WILL BE OPEN, 8 4 8 5 SHORTED, WHEN IN AM INVESTER MODE, TERMINALS 3 8 4 OF 184 WILL BE SHORTED, 8 4 8 5 OPEN. THIS CIRCUIT CAN WITHSTAND IZOVAC OR 28V OC & COMOUCT 0.5 AMPS MAXIMUM. 2 AMP, 125V, MOL-2 1AMP, 250V, AGC-1 10 AMP 250V GI 1838907 7-12-85 T AI WAS 4864-419; RSP ASSY DELETED M PAL ST. ST. AR 15 ROS OF ASSY HI 1838783 9/25/85 R. 2483-258 INDUCTOR, FILTER 4411 - 574 THEN LOCAL FL/ED SWITCH ON AL IS SET TO EQUALIZE, SYSTEM CAN BE PUT-INTO FLOAT REMOTELY BY SHORTING BETWEEN TERMINALS 8 & 7 OF T84. REMOVING THE SHORT WILL PUT THE SYSTEM INTO EQUALIZE. 4411-134 4415-042 4431-135 4431-136 DEL CHARGE CIRCUITRY ADDED RESERVED P.C. CONTROL CARD 4864-732 1830096 10-29-87 POWER SUPPLY & GATE DRIVE CARD WIPER OF S4 (PIN 5) IS +15VOC WITH RESPECT TO COMMON OF A1 (4864-221) EMI SUPPRESSION UPDATED.
L4. CI9. C20, C21, C22 CHANGED.
C3 REMOVED & L3. L6. C1, C2, C30,
C31 ADDED. Gul-5 D 4864-222 A5, A6 R C NETWORK 2861-118 BYPASS 2561-437 K1,2 RELAY . 50A QEC 1 0 1987 BE M RELAY, 48 VOC COTL 2543-797 RELAY 120Y COIL 2546-482 5-19-88 CONNECTION POINT OF CAR C9 CORRECTED PER W. COLLEY OPTION (D) IS PROVIDED WHEN CONSTAC SYSTEM IS WORKING IN CONJUNTION 200A/50MV SHUNT 2982-714 WITH AN EXTERNAL BATTERY CHARGER. EDGEN 5-10-51 300 DH1,258 3.9K, IOW 2656-548 2652-541 POT. R46 ON AT IS ADJUSTED FOR ZERO BATTERY CURRENT WHEN STATIC MAY 2 0 1988 8P M. 2635-680 SWITCH IS OFERATED. ADDED PT. NO. SD5177-319-00 PER O. LYONS. LOGAN'S F-A-F 25 OHM, 10W 2652-427 2615-275 2663-667 33K M2W 5.1 OH, 22W SEP 1 4 1988 BP (7) 10 OHM, 25W 2663-710 2513-852 2513-860 33.Z 100A, SPST 225A,DC DISCONNECT 1 POLE 4 POSITION 2521-143 2 POLE 2 POSITION 2513-316 POWER XEMR SIGNAL XEMR CURRENT XEMR 4561-013 4443-621 2413-211 051 RED L.E.D 2018-127 2945-425 2925-487 2955-415 2955-419 OC VOLTS 0-75 OC AMPS 200-0-200 250Y VARISTOR RVI 2676-547 120Y FAN 181-7095 UL VERSION |~ J3 S.HUSSEY REESE 10-23-81 /-/7-F2 SCHEMATIC DIAGRAM INDICATES THE PART OR ASSEMBLY IS STANDARD ' MODEL WOASOZB LISTED MORE THAN ONCE, USE THE RECOMMENDED SPARE QUANTITY WHERE THE ITEM FIRST APPEARS. SD5177-319-00 SD5177-316-00 LORAIN PRODUCTS CORPORATION LOSAN, OHIO D 8





ISSUE CHANGES & REVISIONS REF. OESI G. UNI T QTY. PART DESCRIPTION UNIT REF. QTY. DESIG. PART DESCRIPTION DESCRIPTION WANDFACTURING REFERENCES 20VAC/DC CATEGORY NUMBER JUN 11 1985 BP 2811-10100 184148 2543-44200 2543-44200 1N4148 1N4148 1N4148 994 R35 R65 R69 2811-10100 2634-69600 2634-69700 2634-69700 2634-69700 2634-69700 /1839494 7-23-86 O.A.C. 95. 3K 95. 3K 95. 3K 133K 165K 165K 165K 165K 165K 221K 48V, RELAY 221 274 825 ASSEMBLY DRAWING 4864-732-00 CZ.CIE WERE 2715-11000. C13.C20 WERE 2715-11300. C7 WAS 2715-12100. C17 WAS 2715-12900 CR22 CR24 CR34 CR3 CR33 CR35 CR35 2811-10100 2811-10100 2811-10100 1N4148 1N4148 1N4148 1/4W 1/4W 1/4W 1/4W 1/4W P.W.BOARD 3156-717-00 2634-59400 R100 R26 R15 R16 2634-70300 2634-70300 1.82K 1.82K 1.82K 1.82K 1.82K 1.82K 1.82K 2 2634-60900 2634-60900 2634-60900 10 1/4W 1/4W 1/4W 1/4W 1/4W 9 Rod. 1/25/45EP 16 1986 MEN RJ PARTY 2811-10100 2811-10100 2811-10100 2811-10100 1N4148 1N4148 1N4148 2634-70800 CONNECTOR ON FRAME C1 1830052 10-7-87 G1 SWI WAS PT. NO. 2513-45300 OCT 22 1987 ... M. JULIU-17 1/4W GRE 2634-60900 2634-60900 2634-60900 2634-70800 2634-70800 2634-70800 2634-70900 2634-71400 1N4148 2811 -10100 2811 -10100 2811 -10100 2811 -10100 2811 -10100 2811 -10100 2811 -10100 2811 -10100 2811 -10100 2811 -10100 2811 -10100 1 H41 48 1 H41 46 1/4W 1/4W 1/4W 1/4W 2634-60900 2634-60900 2634-60900 2634-60900 IN4148 1.82K 1.82K 1.82K 2.74K 2.74K 3.32K 3.32K 3.32K 3.32K 5.11K 5.11K 1/4W 1/4W 1/4W 1/4W 1/4W 1/4W 1/4W 1/4W 2634-71400 1M4148 1M4148 R97 R44 R47 R20 R82 R86 1/4W 2634-71400 2634-71400 2634-71400 2634-71900 2634-61900 2634-61900 2634-62300 2634-62300 1/4W 1/4W 1/4W 1 N4148 1 N4148 1 N4148 1 N4148 1 N4148 1 N4148 R22 R138 CR52 CR53 CR55 1/4W 2634-71900 2634-71900 2634-71900 2634-72300 2634-72300 2634-72300 2634-72300 R63 R77 R167 R21 274K 274K 332K 2634-62300 2634-62300 2634-62300 1/4W 1/4W R98 R98 R104 R156 R157 R158 R159 1/4W 1/4W 1/4W 1/4W CR56 CR57 CR56 CR59 2811-10100 2811-10100 1/4W 332K 332K 332K 499K 499K 499K 2634-63400 2634-63400 2634-63400 1/4W 1/4W 1/4W 1/4W 2811-10100 2811-10100 2811-10100 2811-10100 1N4148 1N4148 1N4148 1N4148 5.11K 5.11K CR60 CR61 CR63 2634-63400 5. 11K 5. 11K 5. 11K 5. 11K 5. 62K 5. 62K 5. 62K 5. 62K 2634-73100 2634-73100 2634-63400 2634-63400 1/4W 2811-10100 1 N41 48 1N4148 1N4148 1N4148 1N4148 1N4148 1N4148 1/4W 1/4W 1/4W 1/4W CR64 CR65 CR65 CR67 2634-73100 2634-63400 1/4W 1/4W 1/4W 1/4W 1/4W 1/4W 1/4W 1/4W 2634-73100 2634-63400 2634-73100 2634-74500 2635-66000 499K 499K 1MEG 4.99K 90.9K 422K 2634-63500 2634-63600 2811~10100 2811-10100 2811-10100 2811-10100 2811-10100 1W 1W 1W 11W 2634-63500 2635-71500 2635-75000 CR7 CR70 1N414B 75 0 R55 R141 2652-45200 2668-60100 1N4148 1N4148 2634-63500 2634-63600 1/4W 1/4W 1/2W, POT CR71 CR72 CR76 2811-10100 2811-10100 2811-10100 2811-10100 2811-10100 2634-63600 2634-63600 2634-64800 1/4W 1/4W 1/4W 1 H41 48 2K 20K 20K 20K 2K 50K 50K 0.033UF 0.1UF 1 N 4 1 4 B 1 N 4 1 4 B 2675-12100 2675-12100 2675-12100 2675-12400 1/2W, POT 1/2W, POT 1/2W, POT 1N4148 2634-64800 2634-64800 2634-64800 2634-64800 2634-64800 2634-64800 CR81 1/2W. POT 1/2W. POT 1/2W. POT 2811-10100 2811-10100 1/4W CR82 CR83 CR20 CR21 CR45 CR9 CR15A CR15B RJB 2675-12600 2675-12600 2713-22400 2715-10600 1N459A 2812-11000 2812-11000 1N459A 1N459A 1N4003 83VDC 2634-64800 2634-64800 2634-64800 2634-64800 2634-64800 2634-64800 2634-64800 2634-64800 2634-64800 C1 C16 1/4W 0. 22UF 0. 22UF 0. 33UF 0. 33UF 100VDC 100VDC 100VDC 2715-11100 2812-33000 2812-33000 1N4820 1N4820 1N4820 1N4820 LEO. RED LED. RED LED. RED LED. RED C20 C7 C17 C25 C15 C12 C14 100VDC 100VDC 100VDC 500VDC 2715-11400 2812-33000 2812-33000 1UF 2. ZUF 330PF 2818-12400 2818-12400 2818-12400 2818-12400 054 057 059 051 053 055 056 2724-12900 1000V0C 50VDC 50VDC 68026 680PF 0. 1UF 10 2724-50900 2724-50900 2818-14100 2818-14100 2818-14100 2818-14100 2818-14100 2816-14100 LED. GREEN LED. GREEN LED. GREEN LED. GREEN 2724-50900 50V0C 2634-64800 2634-64800 2724-50900 1/4W 1/4W 1/4W 1/4W R4 R5G 2724-50900 2724-50900 2724-50900 2634-64800 2634-64800 2634-64800 LED. GREEN 1N4371 CR27 CR6 CR74 CR44 CR47 CR48 CR49 2. 7V. ZENER 3. 3V. ZENER 6. 2V. ZENER 5. 8V. ZENER 6. 8V. ZENER 6. 8V. ZENER 8. 8V. ZENER 8. 8V. ZENER 2724-50900 2724-50900 2834-40500 2835-32500 2634-64800 2634-64800 2634-64800 2634-64800 1N746 1 N4735A 1 N4736A 1 N4736A 1 N4736A 1 N4736A 2 N5088 2835-41100 2835-41100 2835-41100 2724-50900 1/4W 1/4W 1/4W 1/4W 1/4W 2724-54200 2724-54200 2724-54200 470PF 470PF 470PF 2634-64800 2634-65200 2634-65200 2634-65200 2835-41100 2835-41100 2835-41100 2841-03600 2841-03600 2841-03600 2841-03600 2724-55000 2724-55000 2724-55000 IKPF IKPF IKPF IKPF IKPF IKPF 1/4W 1/4W 1/4W 2634-65200 2634-65200 2634-65500 2634-65600 2634-65600 2634-65700 2634-65700 2N5088 2N5088 2N5088 SOVEC NOTES 2724-55000 50VDC 50VDC 50VDC L UNLESS OTHERWISE SPECIFIED. RESISTANCE
VALUES ARE IN DHMS. CAPACITANCE
VALUES ARE IN MICROFARADS. VALUES
PRECEDED BY THE SYMBOL *(PLUS) 1/4W 1/4W 2724-55000 2724-55000 2N5088 2N5087 2N5087 1/4W 1/4W 2724-55000 IKPF IKPF SEOOPF 1/4W 1/4W 1/4W 2843-11000 2634-65700 2634-66400 2634-66400 2634-66400 2634-66400 2634-65400 2634-65400 2534-65400 2534-65600 2534-65600 2534-67000 2634-67000 2N5400 -IMINUSI ARE IN VOLTS 2N5400 2N5400 TI P127 TI P127 TI P127 50VDC 2843-11000 2843-11000 2843-11000 2844-06300 2844-06300 2844-06300 2844-06300 2871-00100 2871-05100 2724-57500 SOVEC Z. CODES SHOWN AS. 2513-453 REPRESENT LORAIN PART NUMBERS 2724-58000 2724-58000 2724-58000 2724-58000 50VDC 50VDC 50VDC 1/4W 1/4W 1/4W 1/4W 50VDC 50VDC 50VDC LOKPF LOKPF LOKPF LZOPF LZOPF LZOPF LZOUF 2724-58000 2724-58000 2724-63800 2724-63800 555 TIMER 368 376 555 II MER FUNCTION GEN OPAMP OPAMP OPAMP OPAMP OPAMP 2871-11500 2871-11500 2871-11500 2871-11500 2871-13100 2871-13100 LOGVOC 1/4W 1/4W 1/4W 1/4W 100VDC 100VDC 40VDC, ELECT. 40VDC, ELECT. 35VDC, TANT. 35VDC, TANT. 35VDC, TANT. 984 A4 A24 2737-10500 2737-12000 2737-12000 2737-12900 O. 22UF LUF LUF 1/4W 1/4W 1/4W 1817332 CADD 2634-67000 C1 4.COLLEY MC14073BCP CA3081 CA3081 2634-68100 2634-68100 1 UF 2. 2UF 1 OUF 2 2UF 2 2UF 2 2UF 2 2UF 2 2UF 4 7UF 1 N4148 1 N4148 2875-32500 2875-40100 2875-40100 2876-10100 1/4W 35 VDC. TANT. 2. CHEVALIER 347E 4-1-85 2737-13700 2534-68100 2534-68100 R148 R155 1/49 2737-13700 2737-13900 A COLLEY 5-11-65 1/4W 1/4W FLIP-FIOR 2634-681G0 2534-68800 FLIP-FLOP CD4538 D. CHE VALIER 2737-13900 SCHEMATIC BIAGRAM 1/4W 1/4W 1/4W 1/4W 2876-30100 6-11-66 2634-69600 CD4011 CD4070 FOR D. J.BECKER 2634-63600 2634-69600 A5 A1 7 I NVERTER
I NVERTER
COUNTER
COUNTER
COUNTER 2876-31000 2876-31000 2875-50100 R14 R144 R168 13900 CJ4 CJ0 CR1 CR10 CR11 CR12 CR13 CR15 CR17 STANDARD CONTROL CARD 2737-14600 2634-69600 2634-69600 2634-69600 2534-69600 1/4W 2611-10100 2811-10100 2811-10100 2811-10100 2611-10100 2611-10100 2811-10100 52 1/4W 1/4W 1/4W 2876-50100 2876-50100 2876-50200 2876-50800 2876-60500 R25 R27 R34 R39 R5 81A 82A 9A 9A 9A 81A 1N4148 1N4148 1N4148 1N4148 2634-69600 COUNTER 1/4W SD4864-732-00 2634-69600 2634-69600 1/4W 1/4W LORAIN PRODUCTS D RELIANCE COMM/TEC ~ 3 3

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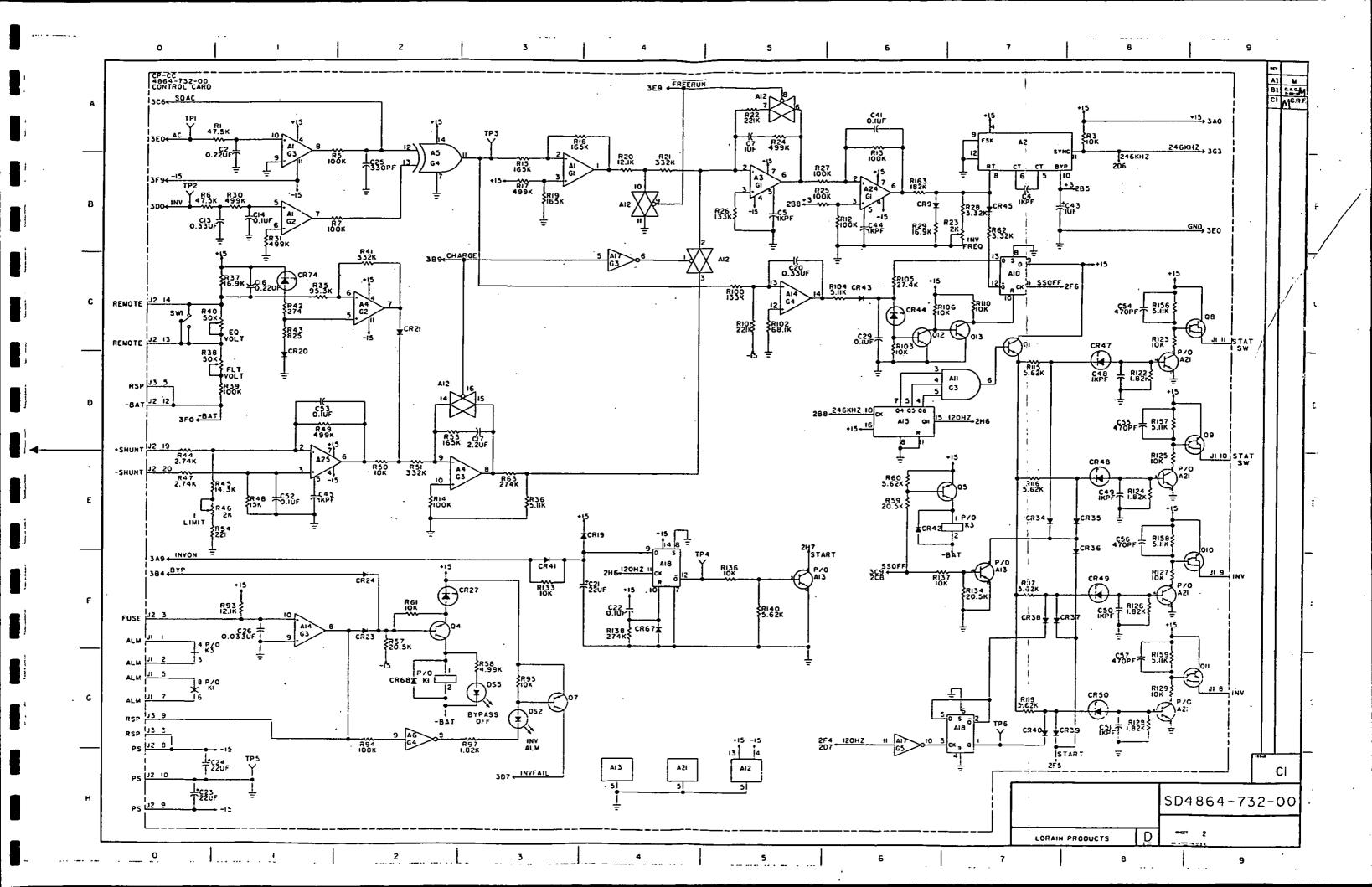
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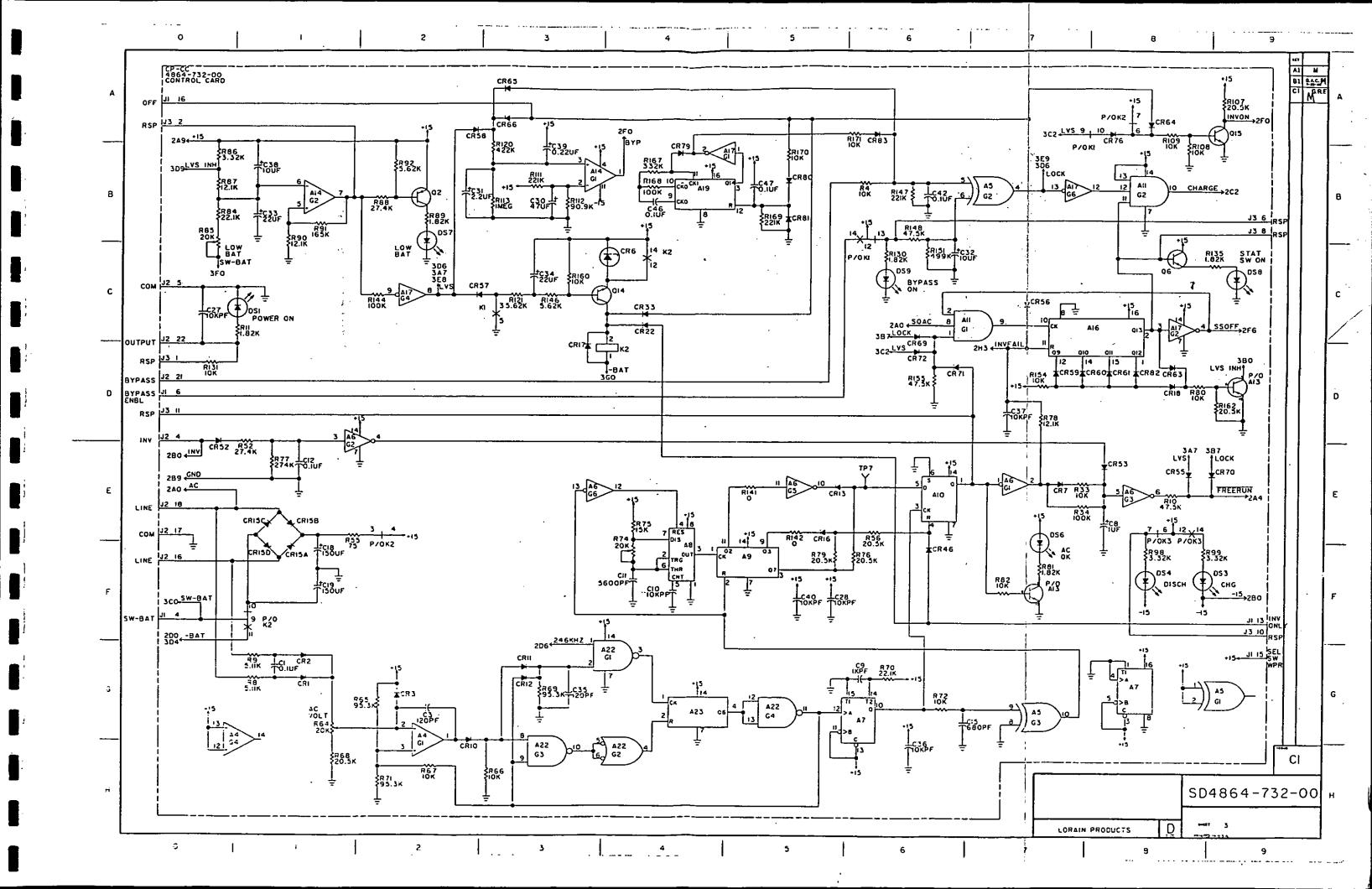
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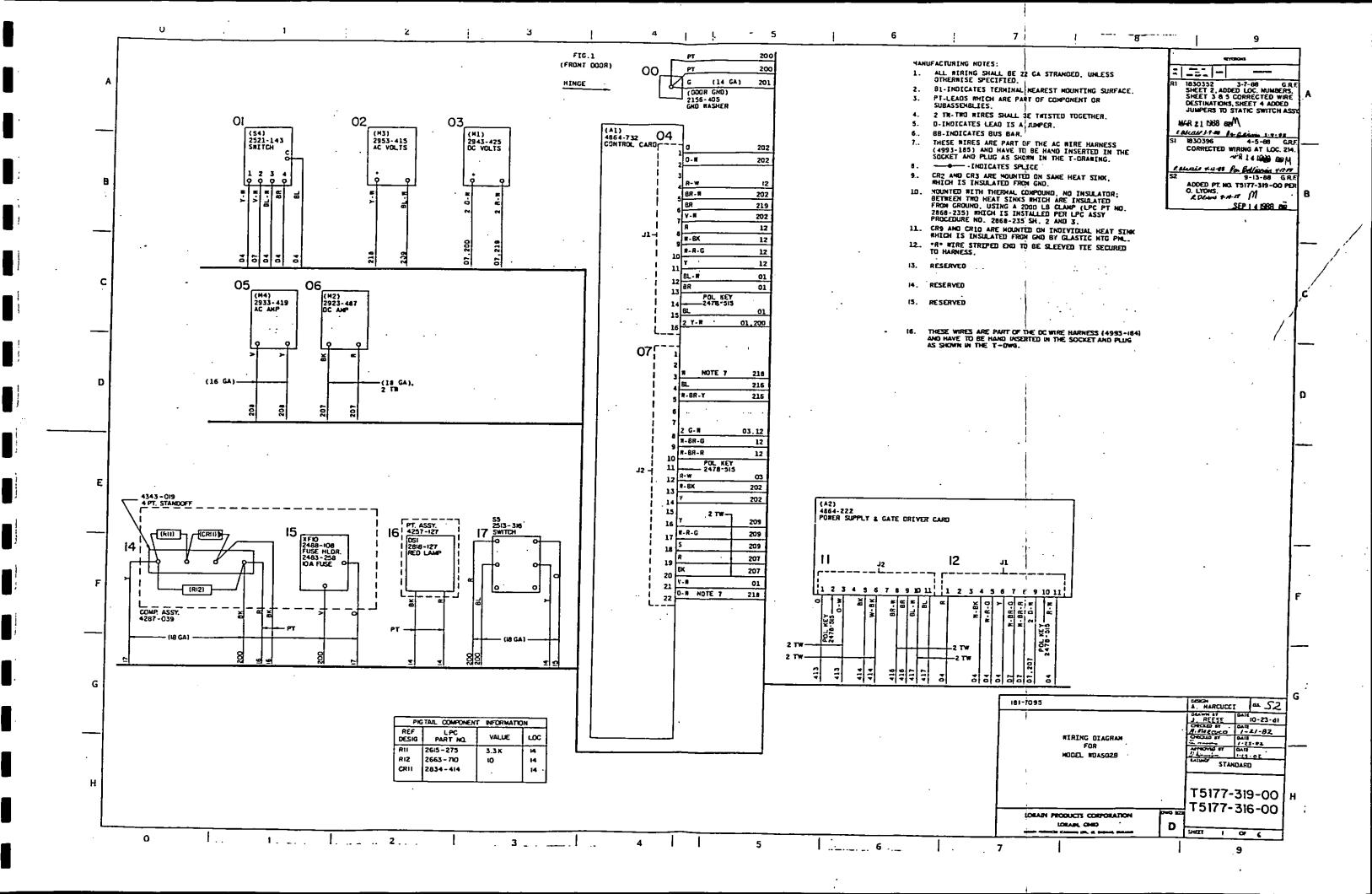
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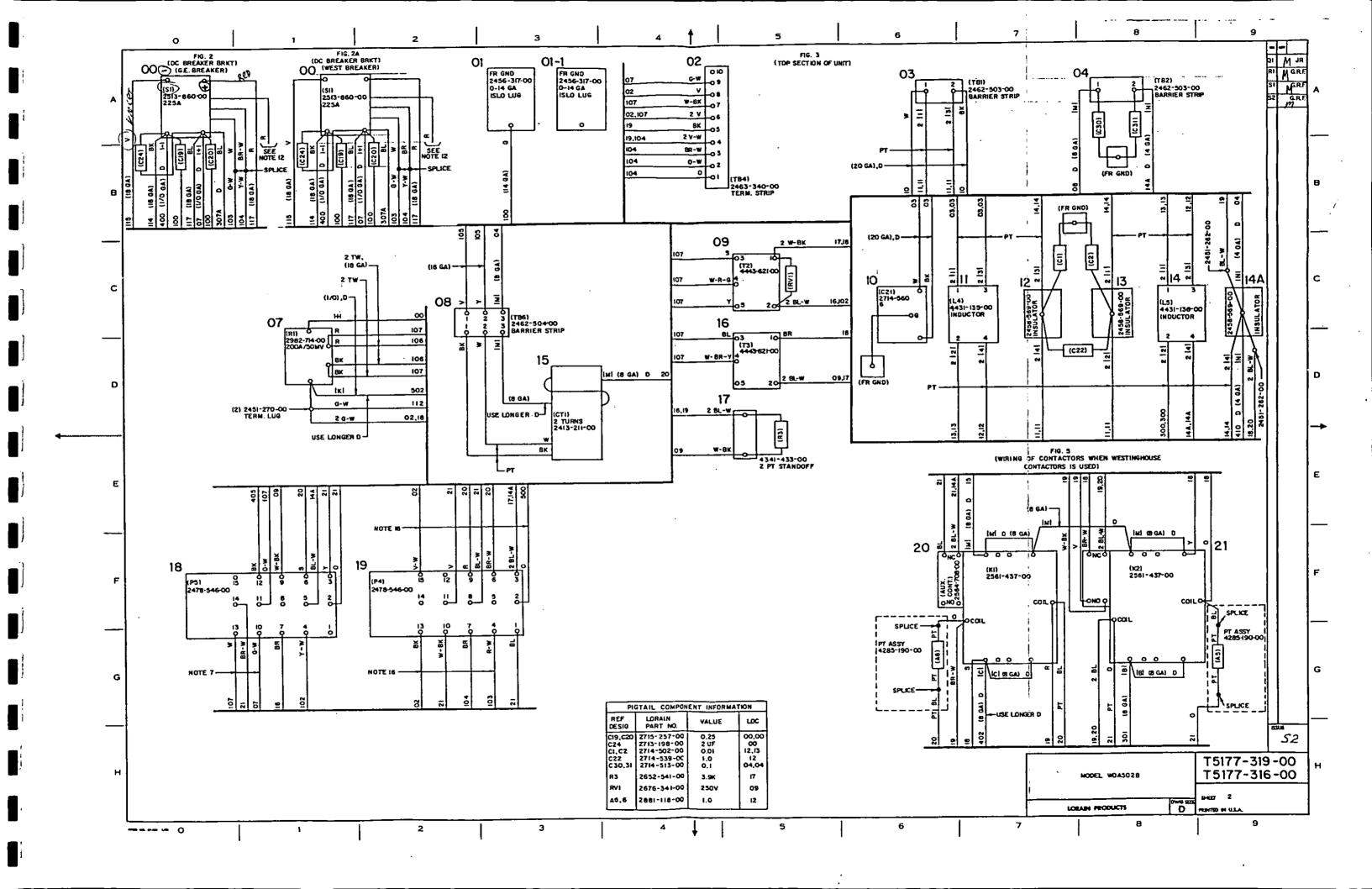
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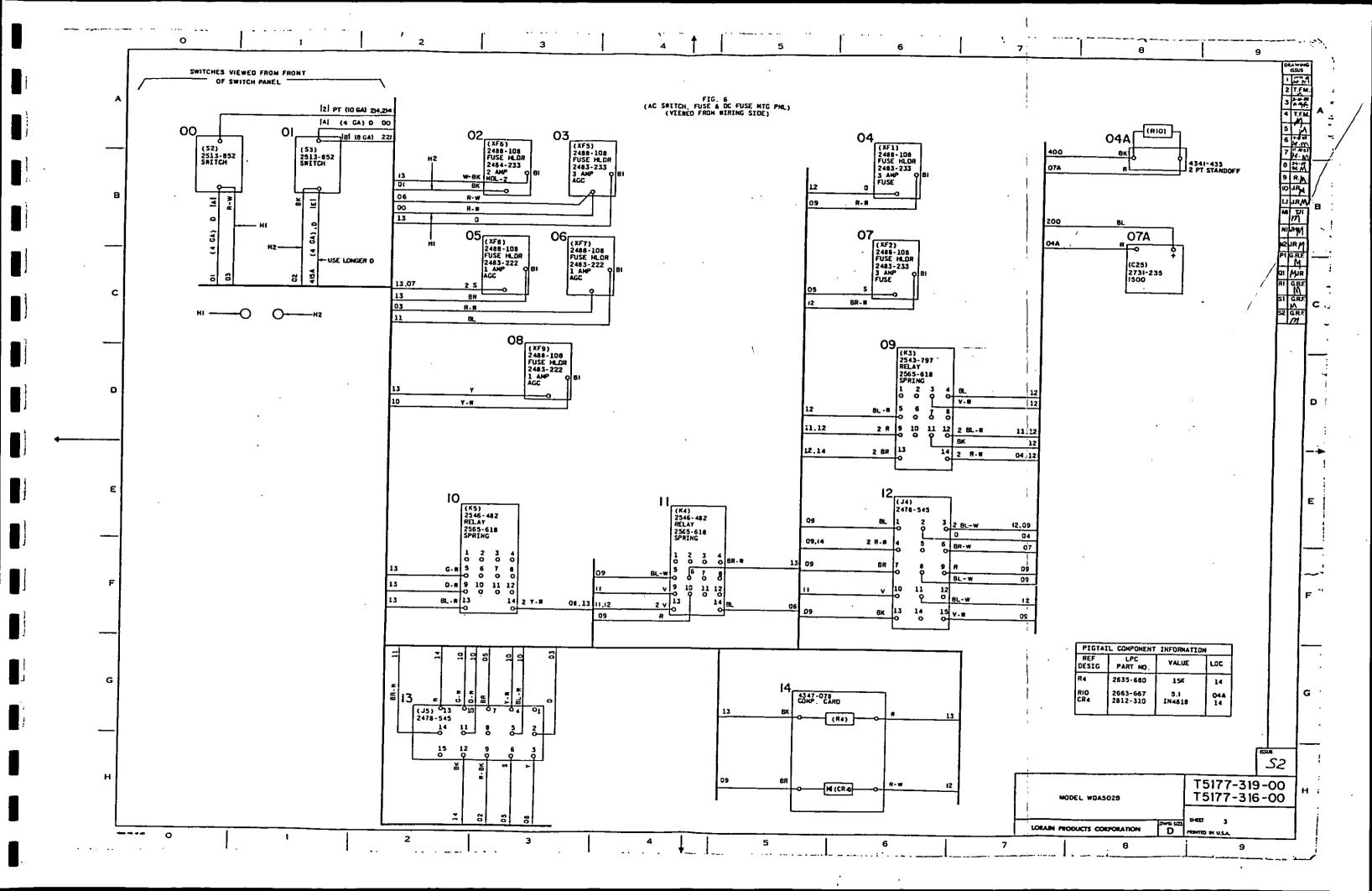
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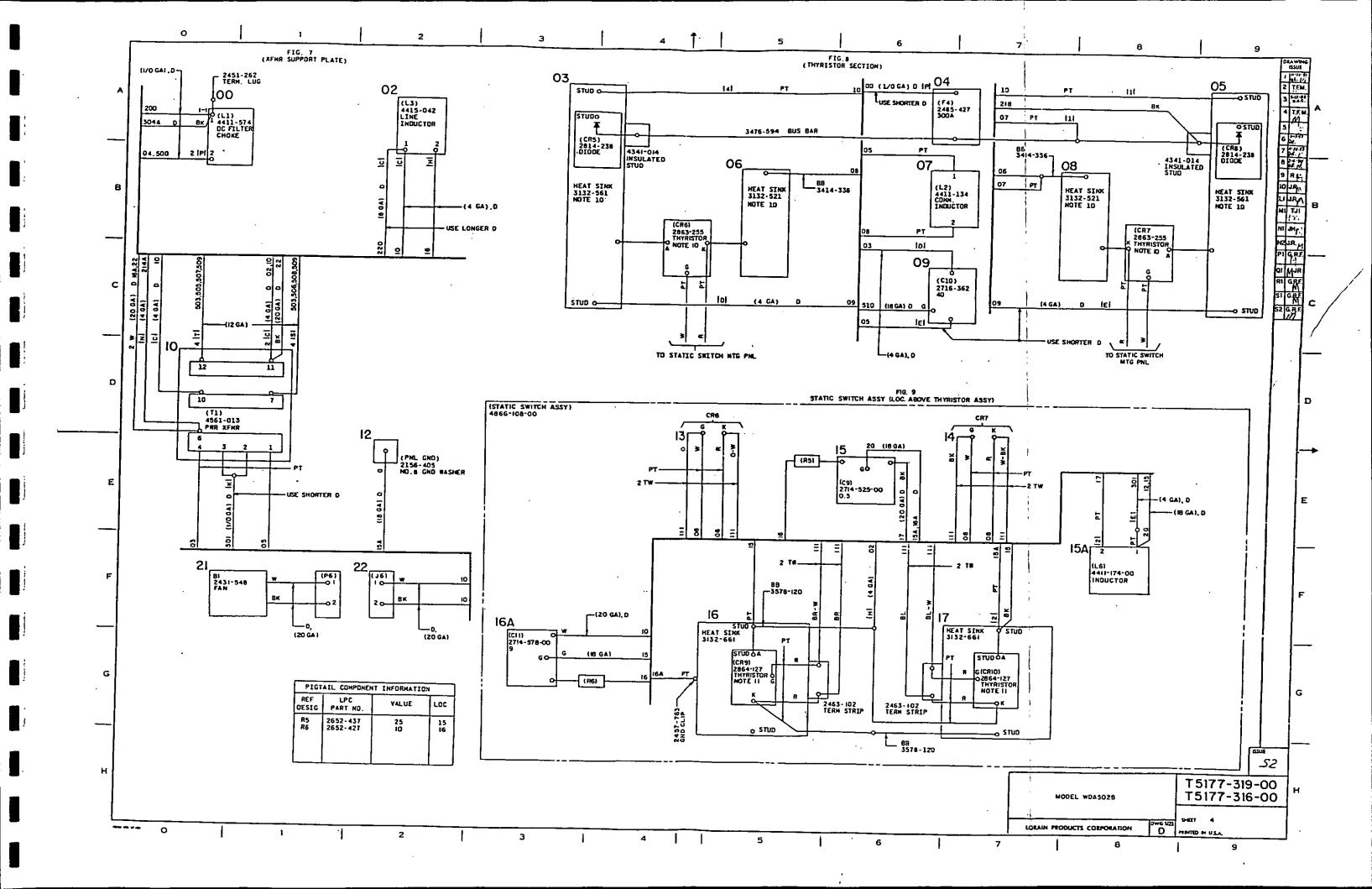


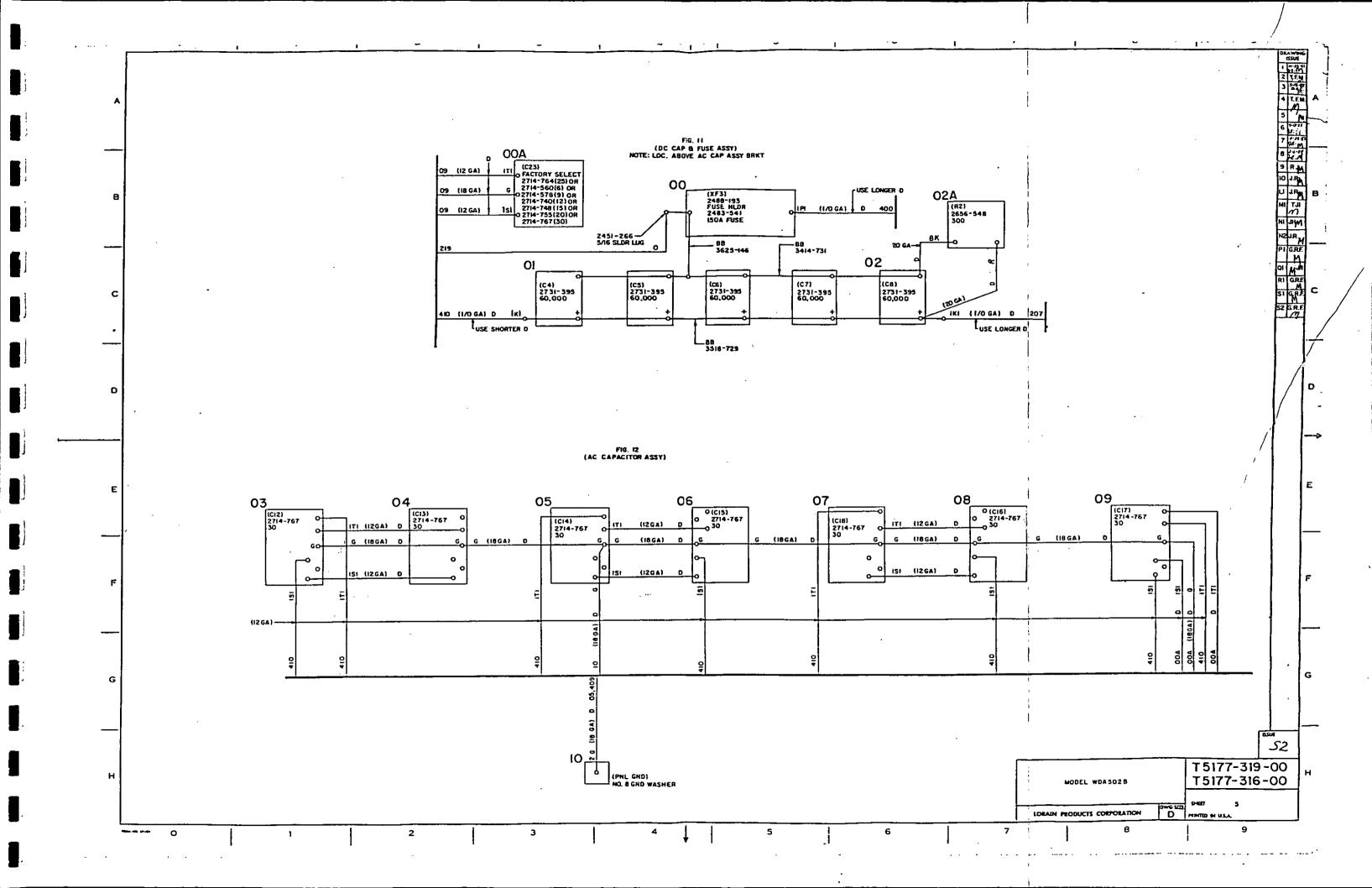


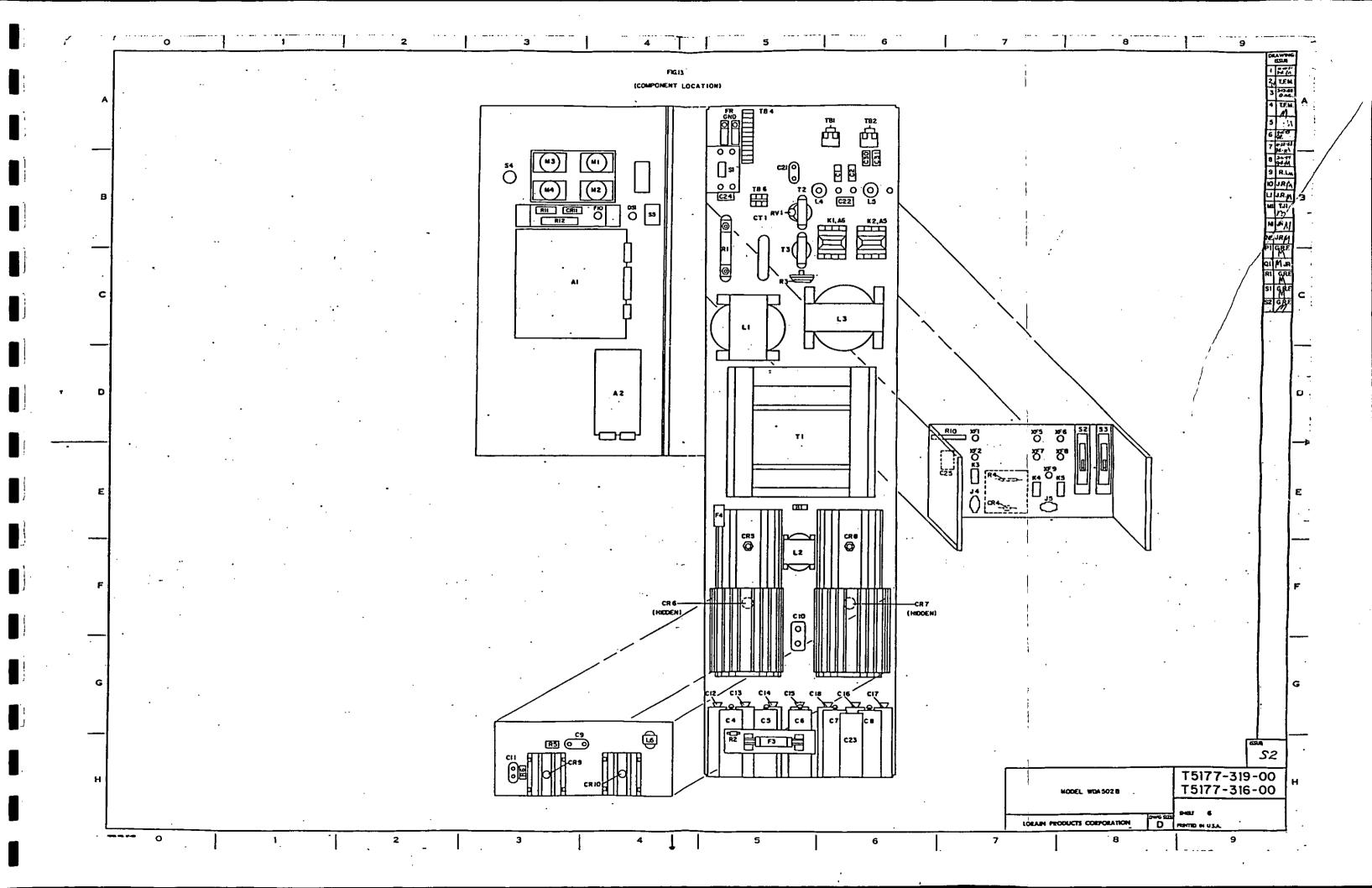












FOR PARTS OR SERVICE, CONTACTTHE NEAREST LORAIN REGIONAL SERVICE CENTER

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Repair Service Power Evaluation Programs (PEP) PowerTherm 5M Thermographic Analysis Product Revision Upgrades COMMENTS:

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- TRAINING
 - **★** Factory
 - **★** Customer Site
 - ★ Slide/Cassette or Video

. FIELD SERVICE REPAIR

- MAINTENANCE CONTRACTS
 - **★** Power Equipment
 - ★ Software/Hardware

- PARTS KITS AND FUSE KITS
- SPARE FUSE HOLDER
- POWER EVALUATION PROGRAM (PEP)
 - ★ Power Analysis for the Entire Power Room
- . SPECIALIZED TEST EQUIPMENT
 - ★ Ringing Generator Test Set-Model LS2000 (Includes loads, metering, digital frequency counter)
 - ★ 130V Converter Test Load Model LS1000 (Variable loads to 6 amps)
- POWERTHERMSM THERMOGRAPHIC SURVEY
 - ★ Infrared Scanning for your Power Room
- POWER UPGRADE AND MODERNIZATION (E & I)
 - ★ Includes engineering, fabrication and complete installation

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